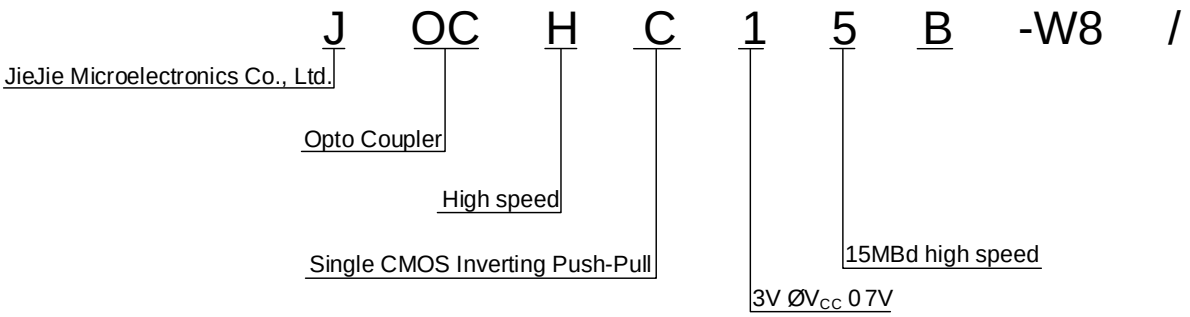
	Time to Logic Low						
	Propagation Delay Time to Logic High	TPLH	I_F=7mA,C_L=15pF	-	55	100	ns
	Common Mode Transient Immunity at Logic High	CM_H	I_F=0mA, V_{CM}=1000Vpp, C_L=15pF, V_{CC}=5V	20	-	-	kV/μs
	Common Mode Transient Immunity at Logic Low	CM_L	I_F=7mA, V_{CM}=1000Vpp, C_L=15pF, V_{DD} '	à CM			

ORDERING INFORMATION



Characteristics Curves

FIG.1:

FIG.7: Propagation Delay vs. Ambient Temperature

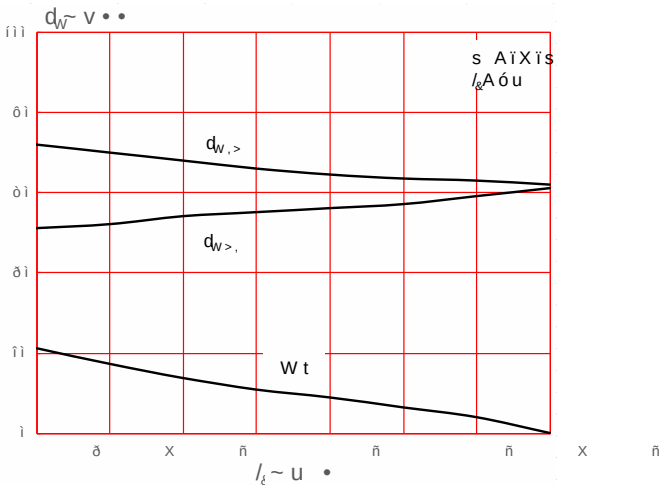
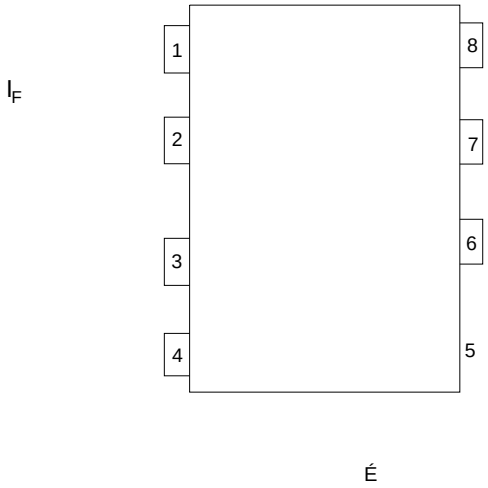


FIG.8: Propagation Delay vs. Ambient Temperature

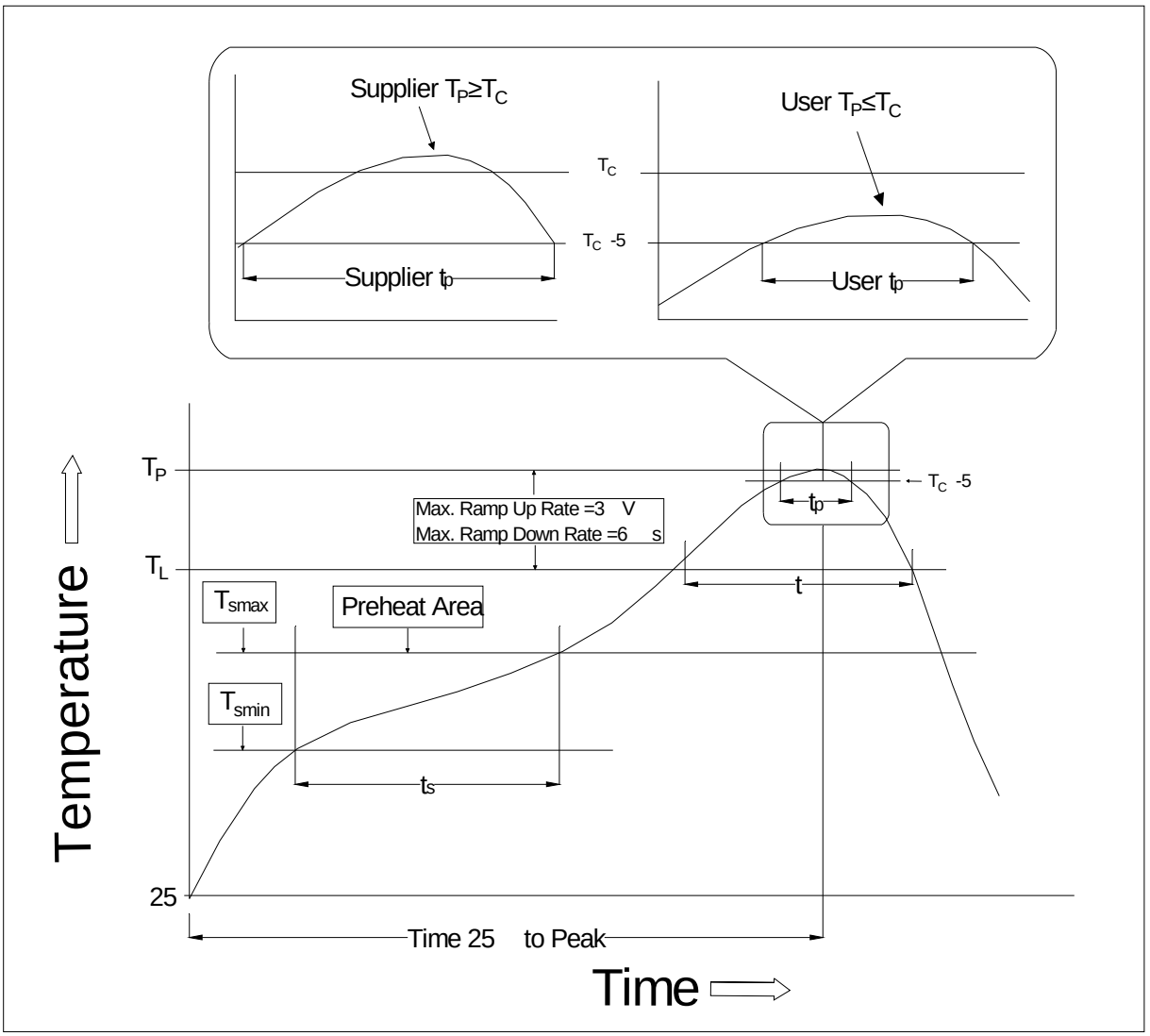
Fig.10: CMTI Test Circuit Recommendations



Package Dimension (Unit: mm)

CARRIER TAPE SPECIFICATIONS (Dim

REFLOW INFORMATION



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150
Temperature Max. (Tsmax)	150	200
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3 /second max.	3 /second max.
Liquidus Temperature (TL)	183	217
Time (tL) Maintained Above (TL)	60-150 seconds	60-150 seconds
Peak Body Package Temperature	235 +0 /-5	260 +0 /-5
Time (tP) within 5 of 260	10 seconds	10 seconds
Ramp-down Rate (TP to TL)	3-6 /second	3-6 /second
Time 25 to Peak Temperature	6 minutes max.	8 minutes max.

Note:

1. Reflow soldering is recommended at the temperatures and times shown, no more than three times.

2. Avoid direct contact between the epoxy body and any tools or surfaces exceeding its maximum storage temperature.

3. Application of pressure on the epoxy body is prohibited at elevated temperatures. In specific scenarios, any applied force must not exceed 2.5N.

4. Ensure the component has cooled to ambient temperature before proceeding with any subsequent manufacturing steps.

5. The component has a shelf life of one year when stored under standard conditions.

6. Recommend storage Temp.: 0~40°C;

Recommend storage humidity: <60%;

MSL level: MSL 1

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