The products are transistor opto-couplers in a plastic SOP4 package. The device combines an AlGaAs inf ‘ diodeas the T Mtter which is optically coupl‘ nar phototransistor detector. WMth the robust coplanar double mol structure, the Tevice provides the most stable isolation feature. The products are widely us switch mode power supplies, program Qble controllers, hous equipment.

High isolation 3750 VRMS
Operating tem eratue ran Þ l M l’ ъ ъ ₴ ⊁‘ ᵐ A M ž Ǝ ъ ₴ ⊁
RoHSM& REACH Compliance
HBM: H3A; MM: M4; CDM:C3
CQCMapprovb
VDEMapprovb
UL approvb
AECQ101 approvb

XTem erature=25°C)

Inpuu‘	Forward Current	I_F	50	mA
	Peak Forward Current	I_{FP}	1	A
	Reverse Volta Þ l	V_R	6	V
	Power Dissi ation	P_D	75	mW
Ouu ɔ φ φ	Collecu A D l’ l ⊀ iu ᵐ l D M ϟ olta Þ	I_{EBO}	V 80	V
	Em tter-collecu A D M ϟ olta Þ l	$_{ECO}$	V 7	V
	Collecu A D M ⊁ φ D rent	I_C	50	mA

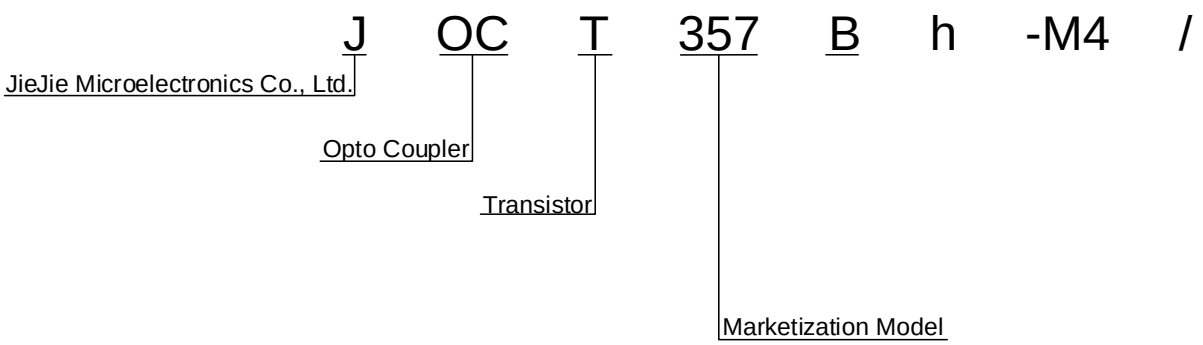
Storage Temperature	T_{stg}	-55~+125	
Soldering Temperature	T_{sol}	260	

: μ :

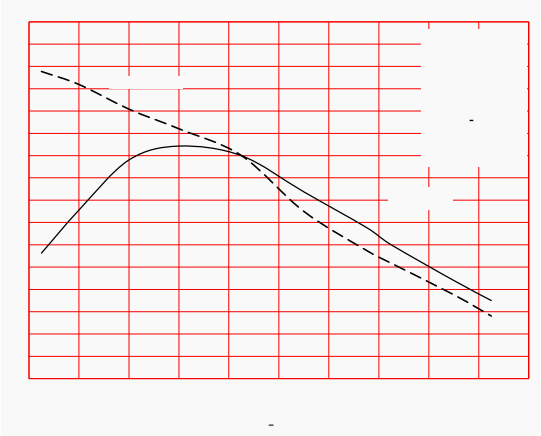
(Temperature=25°C)

Input	Forward Voltage	V_F	I_F=10mA	-	1.2	1.5	V
	Reverse Current	I_R	V_R=6V	-	-	1	μA
	Terminal Capacitance	C_t	V=0, f=1MHz	-	10	-	pF
Output	Collector-Emitter dark current	I_{CEO}	V_{CE}=20V, I_F=0	-	-	100	nA
	Collector-Emitter breakdown voltage	BV_{CEO}	I_C=0.1mA I_F=0	80	-	-	V
	Emitter-Collector breakdown voltage	BV_{ECO}	I_E=0.1mA I_F=0	7	-	-	V
Transfer Characteristics	Current transfer ratio	CTR	I_F=5mA V_{CE}=5V	80	-	600	%
	Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I_F=20mA I_C=1mA	-	0.06	0.2	V
	Isolation resistance	R_{IO}	DC500V 40~60%R.H.	10^{12}	10^{14}	-	Ω
	Floating Capacitance	C_{IO}	V=0, f=1MHz	-	0.4	1	pF
	Cut-off Frequency	f_c	V_{CE}=5V, I_C=2mA R_L=100Ω, -3dB	-	80	-	kHz
	Rise Time	t_r	V_{CE}=2V, I_C=2mA R_L=100Ω	-	3	18	μs
	Fall Time	t_f		-	4	18	μs
	Response Time	t_{on}		-	6	25	μs
		t_{off}		-	5	25	μs

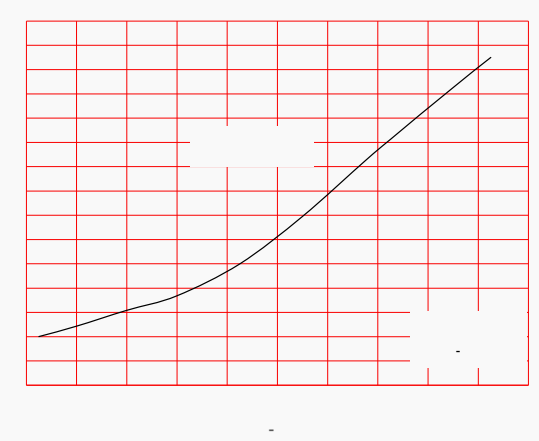
:


J OC T 357 B h -M4 /
JieJie Microelectronics Co., Ltd.
Opto Coupler
Transistor
Marketization Model

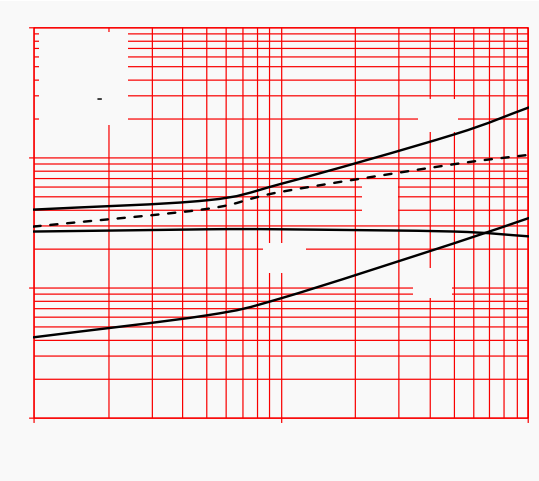
Normalized Current Transfer Ratio vs. Ambient Temperature



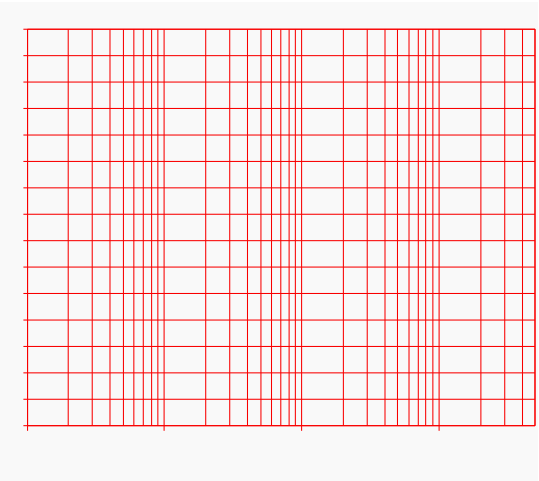
Normalized Collector-emitter Saturation Voltage vs. Ambient Temperature



Response Time vs. Load Resistance

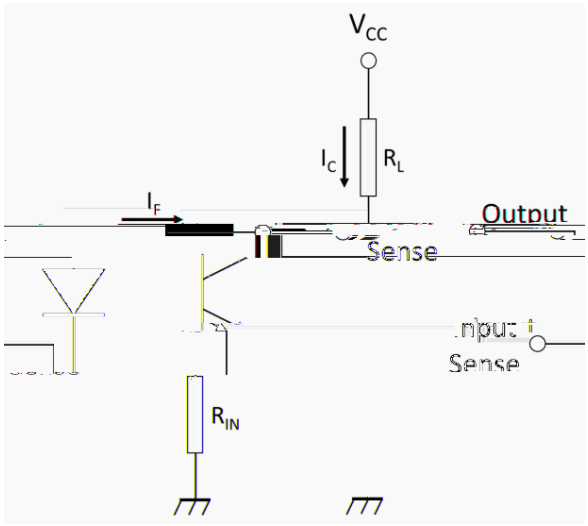


Frequency Response

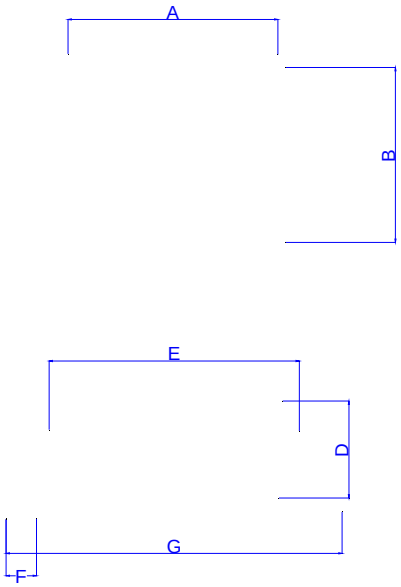


Test Circuits of Response Time

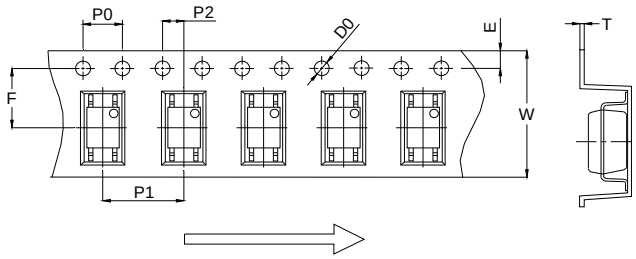
Curves of Response Time



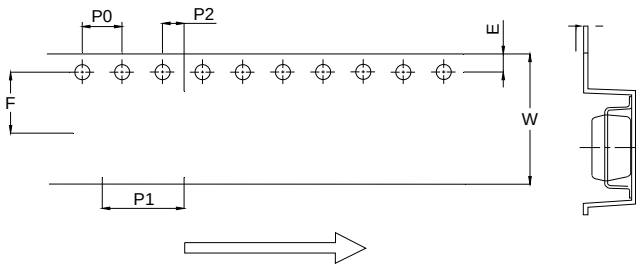
Test Circuits of Frequency Response

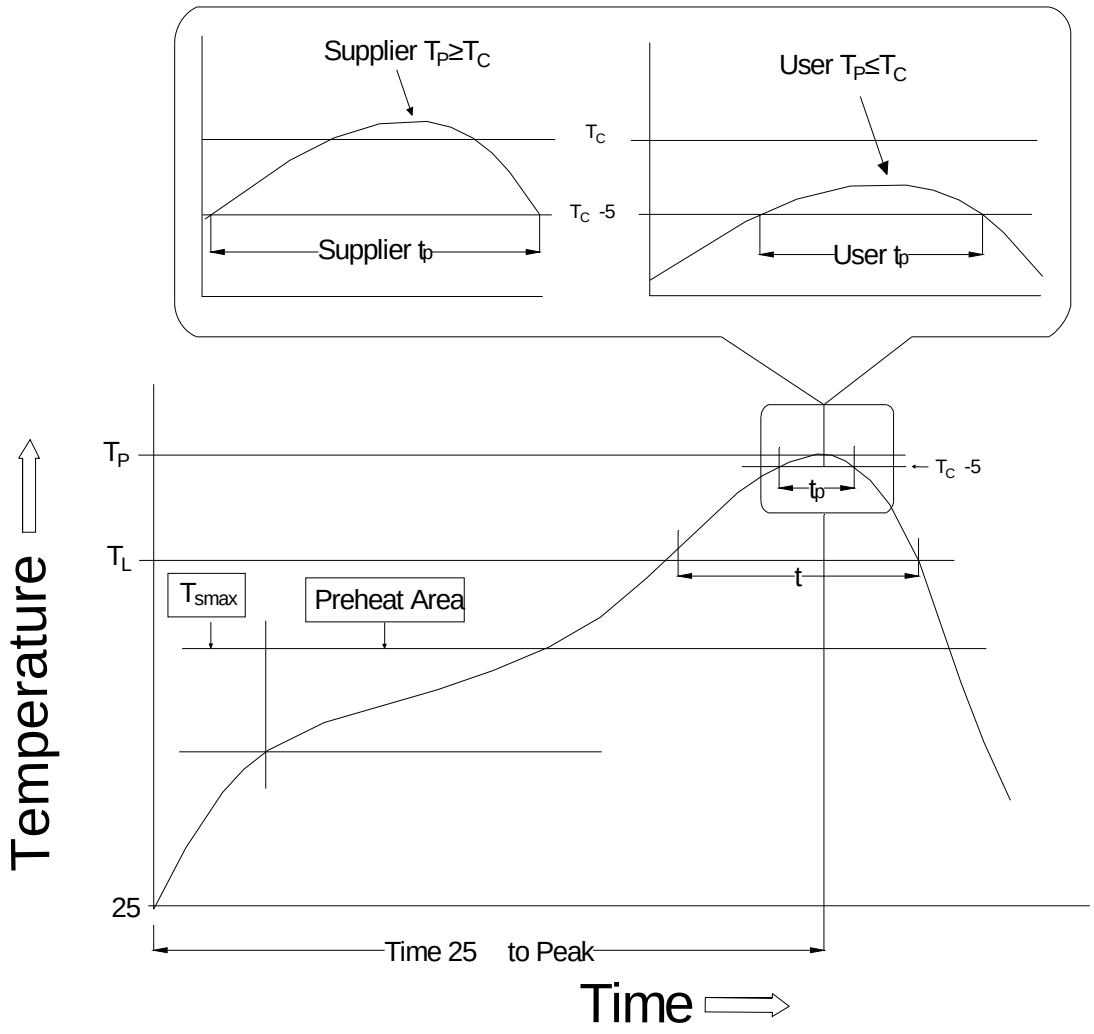


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.80	0.173		0.189
B	3.60		4.20	0.142		0.165
C						
D	1.90		2.30	0.075		0.091
E						
F	°	°.	.0	Ô0	0	0 . 1 þ
G	6.70			0.264		
I						



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
D0		1.50	1.60		0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
E	1.65	1.75	1.85	0.065	0.069	0.073
F	4.40	4.50	4.60	0.173	0.177	0.181
T	0.25	0.30	0.35	0.010	0.012	0.014
W	11.90	12.00	12.30	0.469	0.472	0.484




Supplier $T_P \geq T_C$
User $T_P \leq T_C$
T_C
T_C -5
Supplier t_p
User t_p
T_P
T_L
T_{smax}
Preheat Area
T_C -5
t_p
t
25
Time 25 to Peak
Temperature
Time

Note:

1. Reflow soldering is recommended at the temperatures and times shown, no more than three times.

2. Avoid direct contact between the epoxy body and any tools or surfaces exceeding its maximum storage temperature.

3. Application of pressure on the epoxy body is prohibited at elevated temperatures. In specific scenarios, any applied force must not exceed 2.5N.

4. Ensure the component has cooled to ambient temperature before proceeding with any subsequent manufacturing steps.

5. The component has a shelf life of one year when stored under standard conditions.

6. Recommend storage Temp.: 0~40°C;

Recommend storage humidity: <60%;

MSL level: MSL 1

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2026 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.