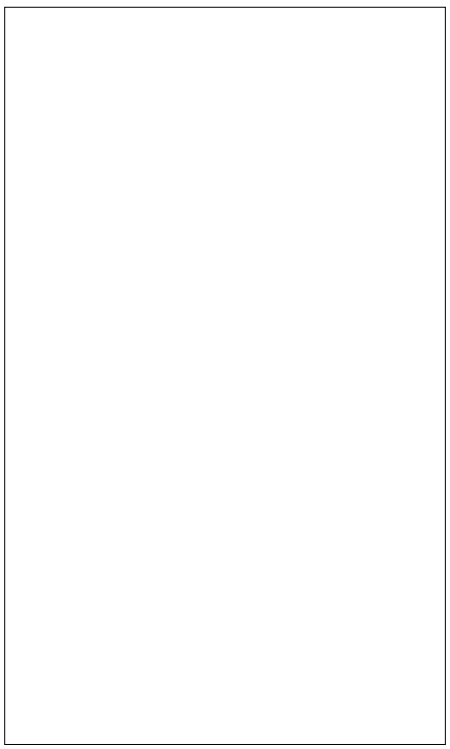
ACJT410-8K 4A TRIAC



Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	3	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3k	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	30	mA
				45	
dV/dt	V_D=540V Gate Open T_j =125		MIN.	500	V s
			MIN.	3	A/ms
	mA		TYP.	2.5	s
				25	
			MIN.	850	V

er	Value(MAX.)	Unit
j=25	1.55	V
j=125	0.73	V
j=125	171	
j=125	0.25	mA

er	Value	Unit

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

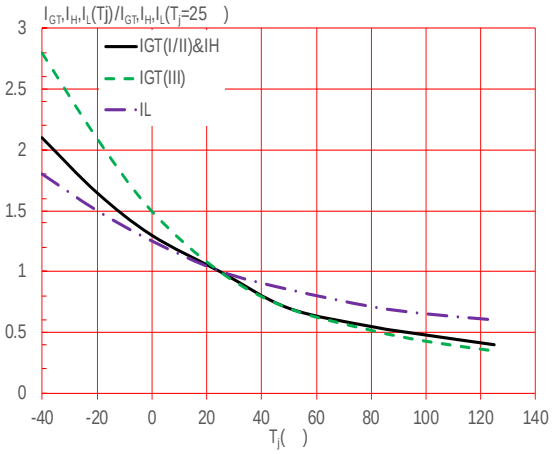
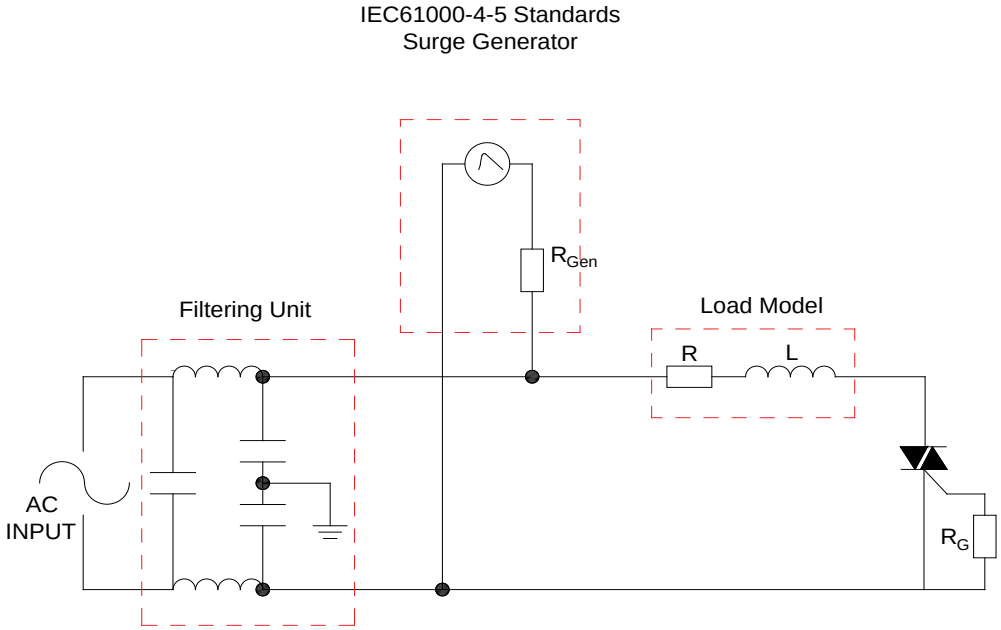
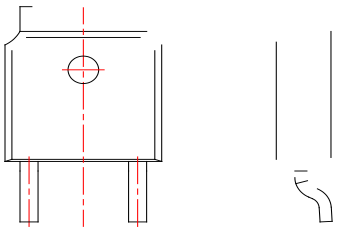


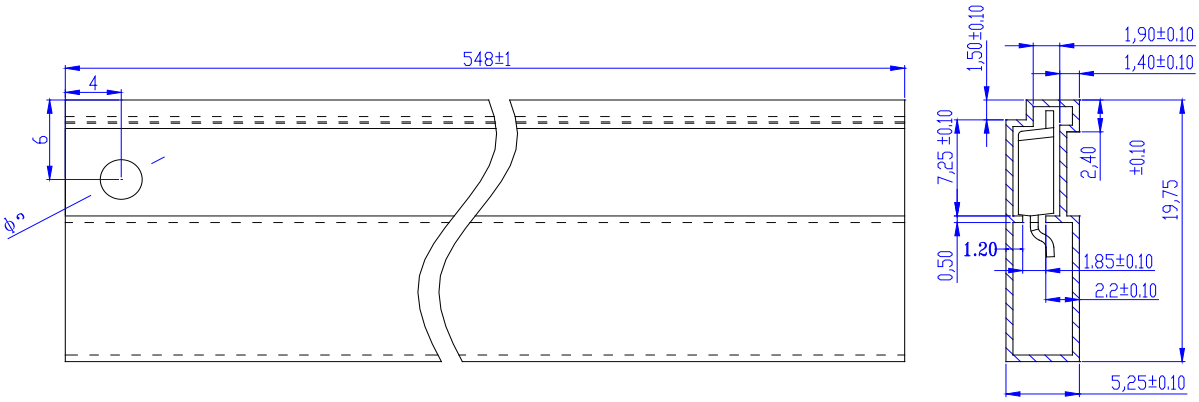
FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty.



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065


548±1
4
6
1,90±0.10
1,40±0.10
1,50±0.10
7,25 ±0.10
2,40
±0.10
19,75
0,50
1.20
1.85±0.10
2,2±0.10
5,25±0.10

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