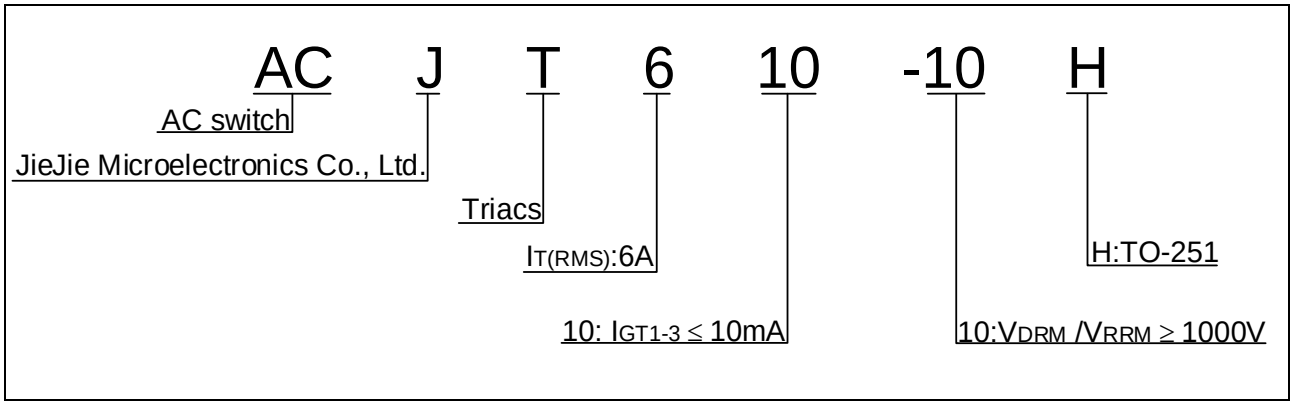
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	3.5	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	20	mA
				35	
I_H	I_T =100mA		MAX.	20	mA
dV/dt	V_D=670V Gate Open T_j =125		MIN.	300	V/μs
(dI/dt)c	(dV/dt)c=10V/μs, T_j=125		MIN.	3	A/ms
t_{on}	I_G=20mA I_A=200mA I_R=20mA T_j=25		TYP.		


AC J T 6 10 -10 H
AC switch
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):6A
10: IGT1-3 ≤ 10mA
10:VDRM /VRRM ≥ 1000V
H:TO-251

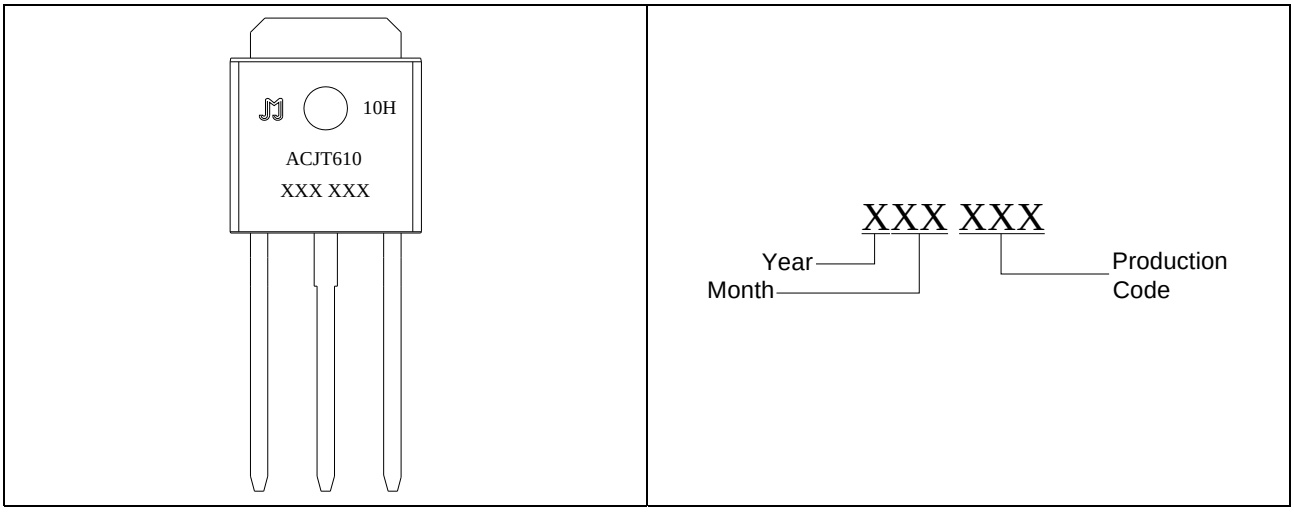

10H
ACJT610
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

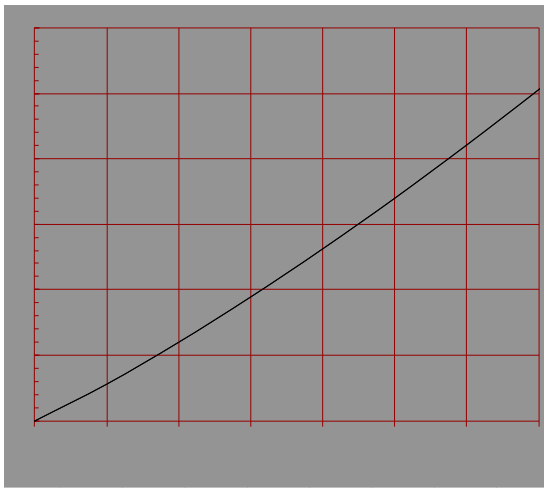


FIG.2: RMS on-state current versus case temperature

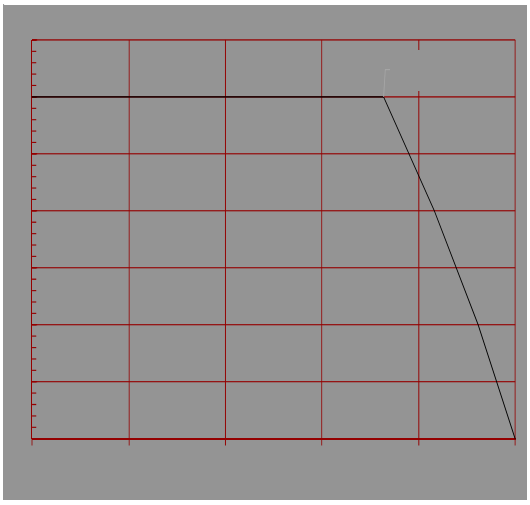


FIG.3: Surge peak on-state current versus number of cycles

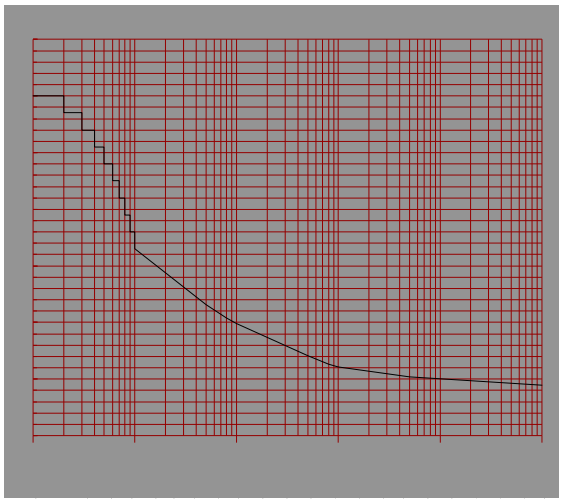
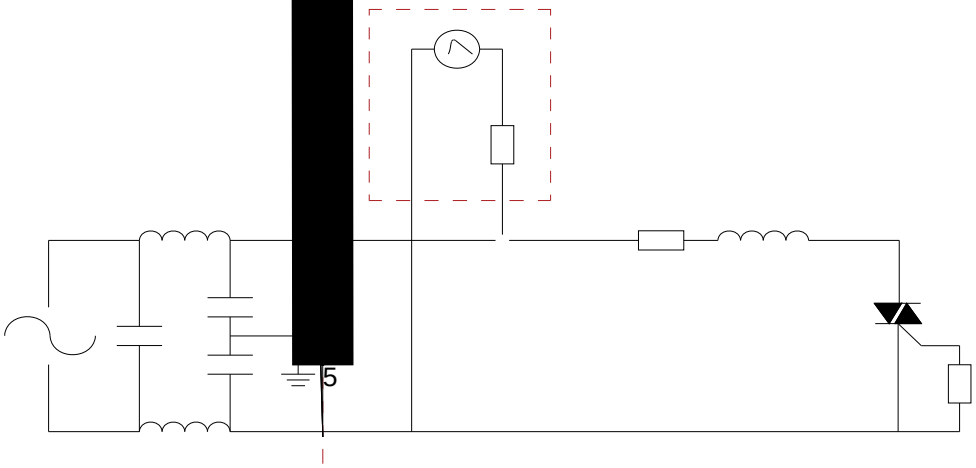
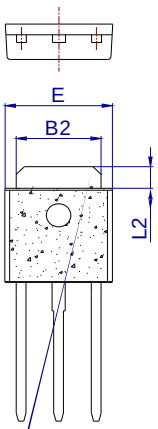


FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery


E
B2
L2

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