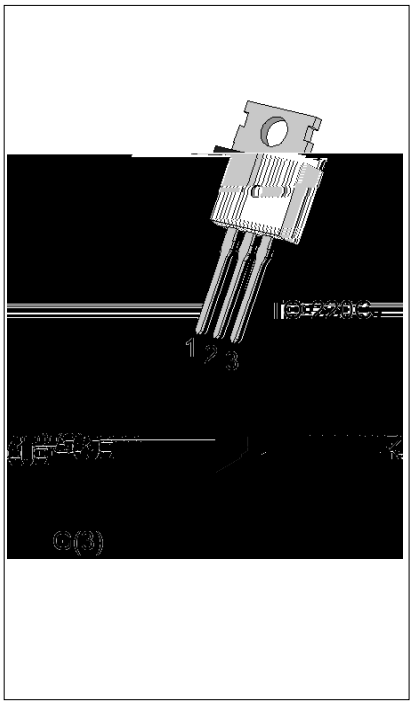
JCT1208C 8A SCR

Rev.A.1.1

DESCRIPTION:

With high ability to withstand the shock loading of large current, JCT1208C provides high dV/dt rate with strong resistance to electromagnetic interference. It is especially recommended for use on hair straightener motorcycle voltage regulator etc. Package TO-220C is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	1200	V
I_{GT}	≤15	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
Average on-state current ($T_C \leq$ 103)	$I_{T(AV)}$	5	A
RMS on-state current ($T_C \leq$ 103)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (t_p=10ms, T_j=25)	I_{TSM}	85	A
Non repetitive surge peak on-state current (t_p=8.3ms, T_j=25)		94	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	36	A^2s
Critical rate of rise of on-state current (I_G=2 I_{GT}, f=100Hz , T_j=125)	dI/dt	100	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	4	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	5	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	1	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

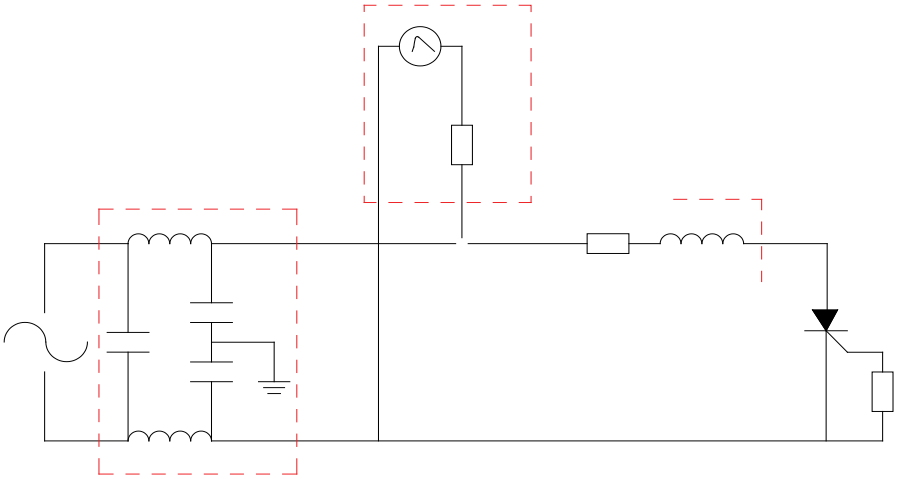
Symbol	Test Condition	Value	Unit

JCT1208C

FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.