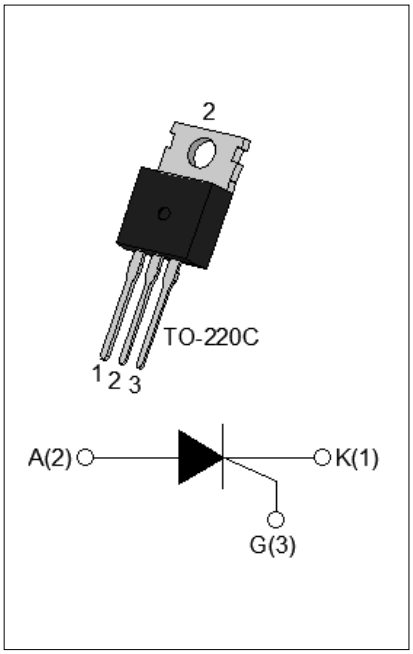
JCT1240C 40A SCR

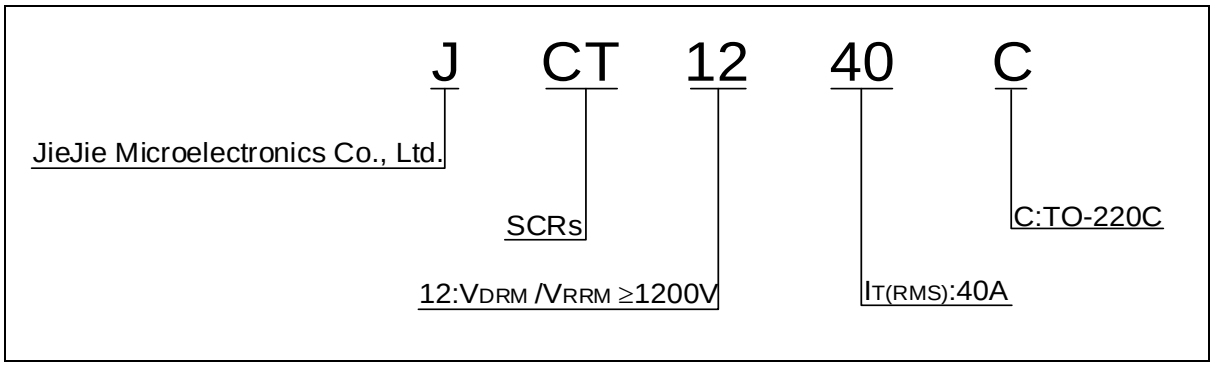
Rev.A.1.1

With high ability to withstand the shock loading of large current, JCT1240C SCR provides high dV/dt rate with strong resistance to electromagnetic interference.It is especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc. Package TO-220C is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	40	A
V_{DRM}/V_{RRM}	1200	V
I_{GT}	≤45	mA

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
Average on-state current (T_C 96)	$I_{T(AV)}$	25	A
RMS on-state current (T_C 96)	$I_{T(RMS)}$	40	A
Non repetitive surge peak on-state current (t_p=10ms, T_j=25)	I_{TSM}	500	A
Non repetitive surge peak on-state current (t_p=8.3ms, T_j=25)		540	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	1250	A^2s
Critical rate of rise of on-state current (I_G=2 I_{GT}, f=100Hz , T_j=125)	dI/dt	200	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	10	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	1	W


J
CT
12
40
C
JieJie Microelectronics Co., Ltd.
SCRs
12:VDRM /VRRM ≥1200V
IT(RMS):40A
C:TO-220C

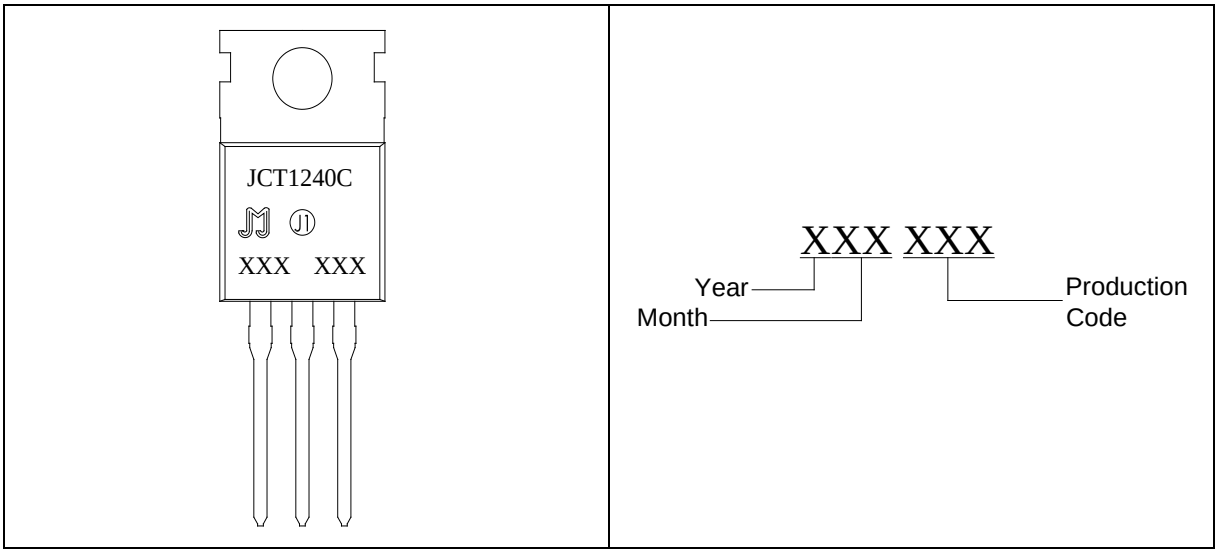

JCT1240C
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

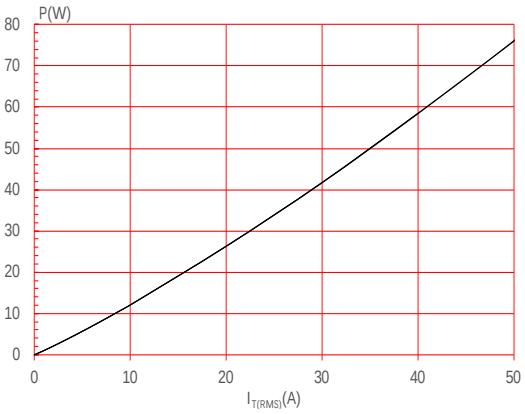


FIG.2: RMS on-state current versus case temperature

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT1240C	1200	45	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update
Oct.14, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA



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