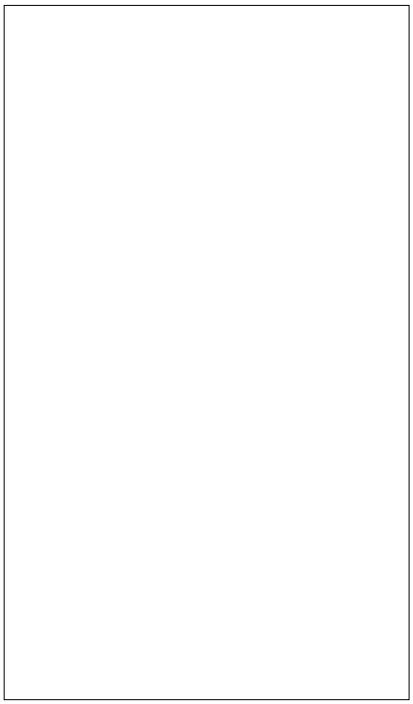
JCT1612C 12A SCR

Rev.A.1.1

DESCRIPTION:

With high ability to withstand the shock loading of



Peak gate power	P_{GM}	5	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	1.8	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	V_D=12V R_L=33Ω	-	-	15	mA
V_{GT}		-	-	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	0.2	-	-	V
I_L	I_G=1.2I_{GT}	-	-	40	mA
I_H	I_T=500mA	-	-	30	mA
dV/dt	V_D=1070V Gate Open T_j=125	3000	-	-	V/μs
t_{on}	I_G=20mA I_A=200mA I_R=20mA T_j=25	-	3	-	μs
t_{off}		-	40	-	μs

STATIC CHARACTERISTICS

Symbol	Parameter	Value(MAX.)	Unit
V_{TM}	I_{TM}A>Tj CS2 cs 12 0 0 scn 1.793 0 Td ( )Tj CS0 cs 0 scn 50.28 0 Td tl		

ORDERING INFORMATION

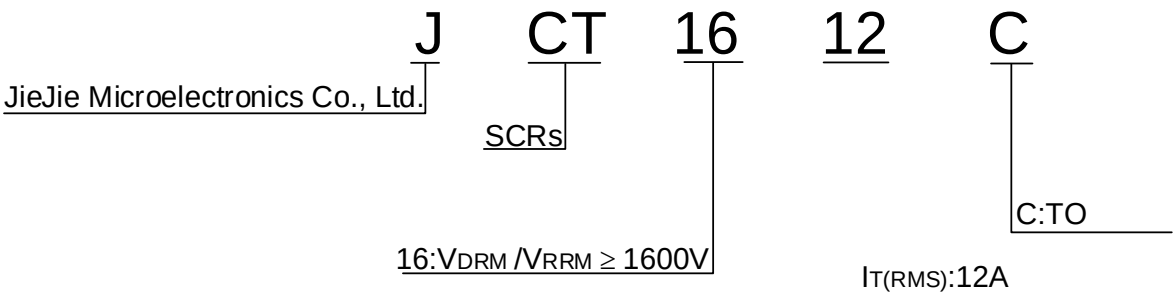


FIG.1: Maximum power dissipation versus RMS on-state current

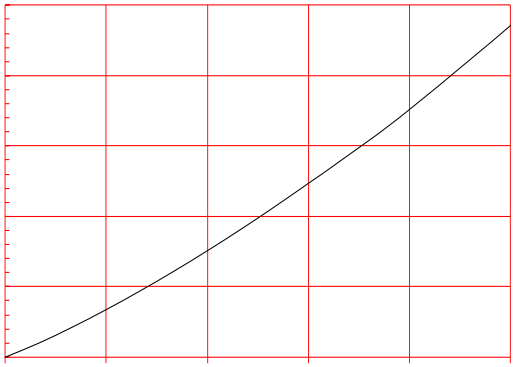


FIG.2: RMS on-state current versus case temperature

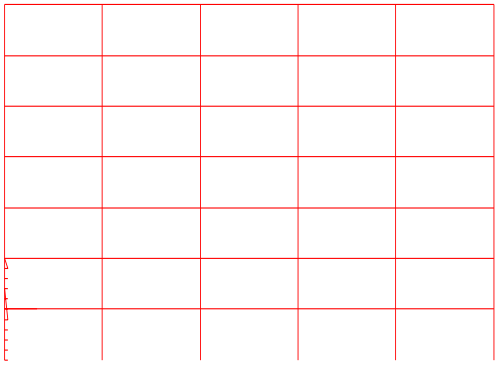
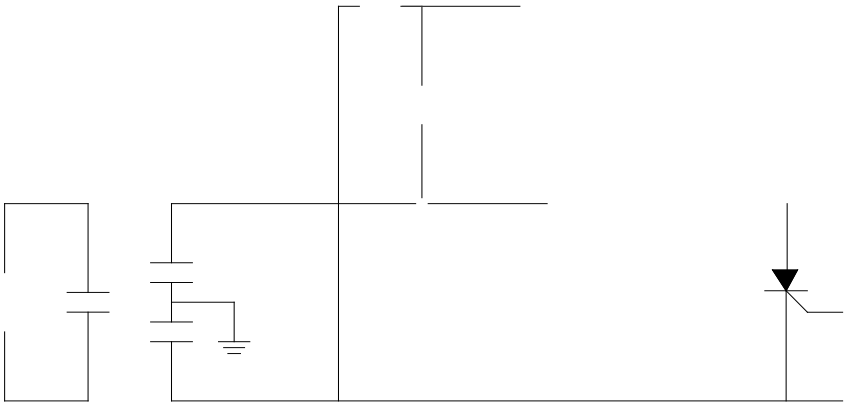


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



ORDERING INFORMATION

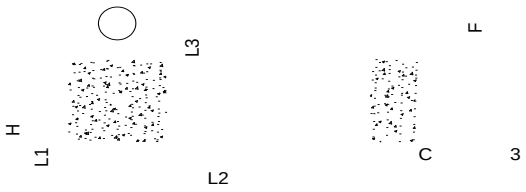
Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT1612C	1600	15	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Aug.10, 2023	A.1.0	Last update

PACKAGE MECHANICAL DATA

i s n e m i D Inch



C

Ref.

Min. Typ.

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