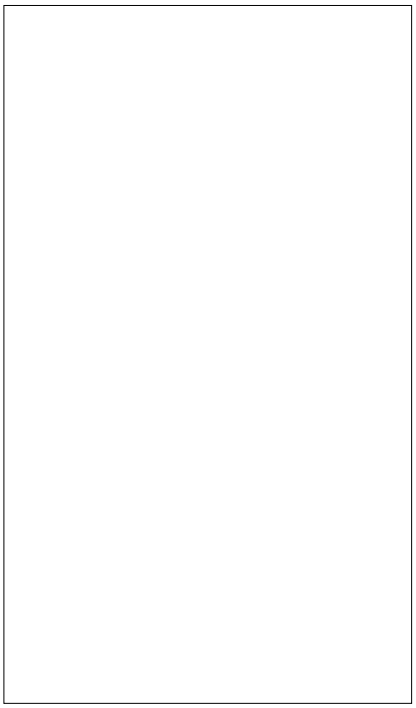
DESCRIPTION: e r48(S0 Tc 0 Tw 4 0 T6.735 ( )Tj 0.222/P <<



Average gate power dissipation (T_j=125℃)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	5	W
Peak pulse voltage (T_j=25℃; non-repetitive,off-state;FIG.8)	V_{pp}	1.8	kV

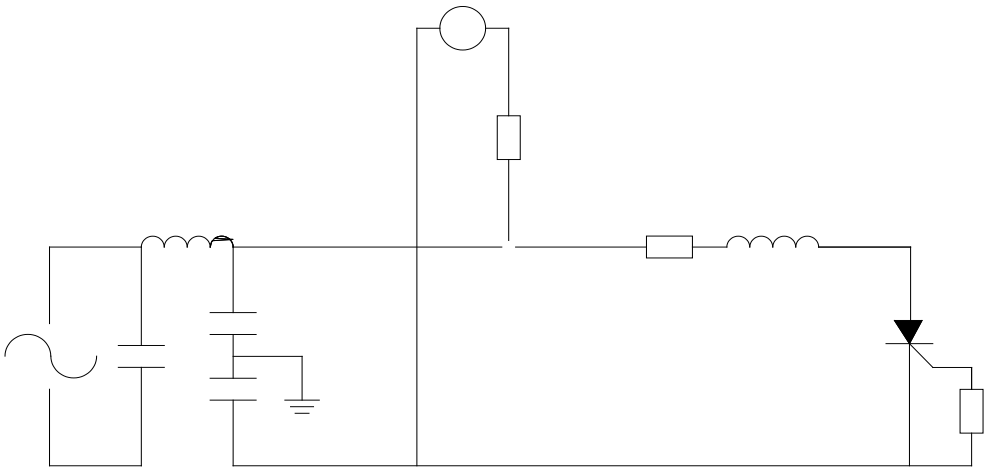
ELECTRICAL CHARACTERISTICS (T_j=25℃ unless otherwise specified)

I_{GT}	V_D=12V R_L=33	-	-	15	mA
V_{GT}		-	-	1	V
V_{GD}	V_D=V_{DRM} T_j=125℃ R_L=3.3k	0.2	-	-	V
I_L	I_G=1.2I_{GT}	-	-	40	mA

ORDERING INFORMATION

J CT 16 1269

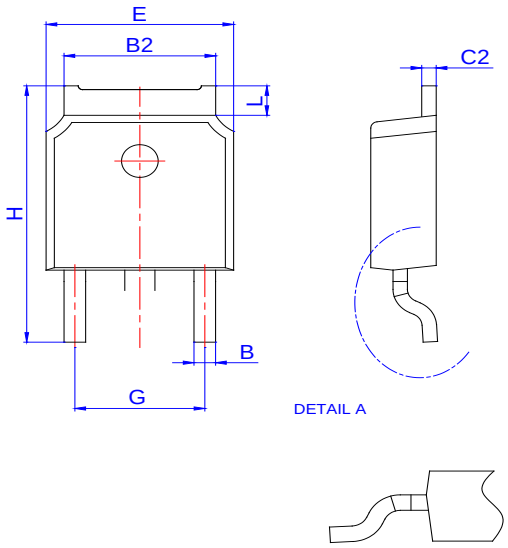
FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



ORDERING INFORMATION

Date	Revision	Changes
Aug.10, 2023	A.1.0	Last update
Oct.21, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065



(XMOs 22.07 Aq /GS0 gs n q /GS0 gs 26.829 ( )13.7 (6 Tm 97 /InlineSha2.3