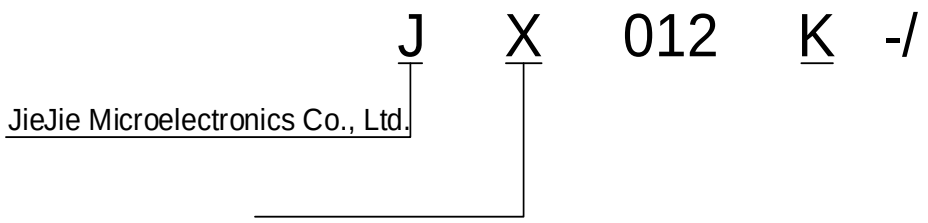
Peak gate power	P_{GM}	2	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	1	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	V_D=12V R_L=33	-	50	200	A
V_{GT}		-	0.6	0.8	V
V_{GD}	V_D=V_{DRM} T_j=110	0.2	-	-	V
I_L	I_G=1.2 I_{GT}	-	-	4	mA
I_H	I_T=0.05A	-	-	3	mA

i (j)Tt16 Tm (1.7
/[(0.dt (05)]TJ 0

ORDERING INFORMATION



JX012K Jie

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

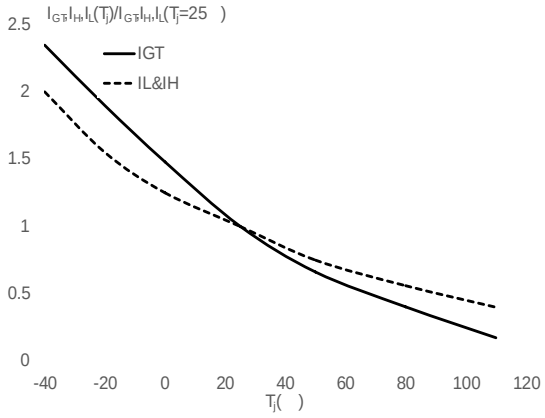
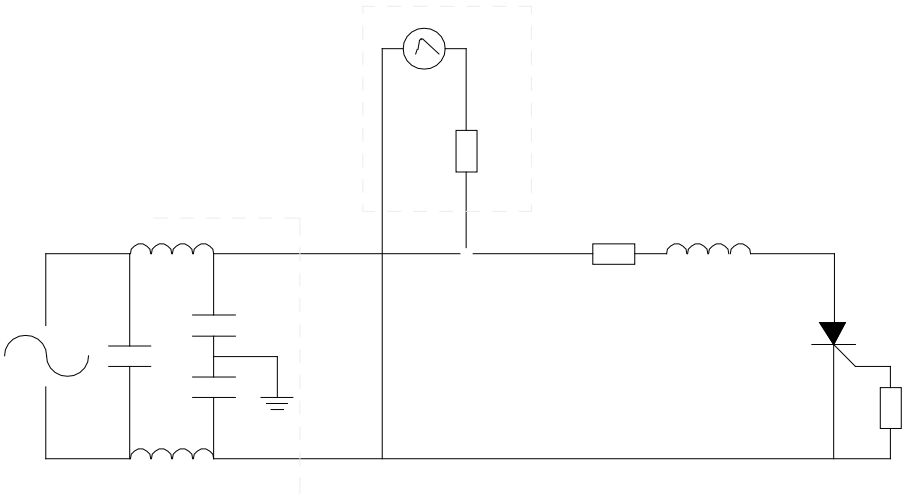


FIG.8 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards.



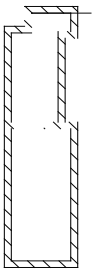
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT**(μA**)	Package	Base qty. (pcs)	Delivery mode
JX012K	1200	**≤200**	TO-252	80	Tube
JX012K-TR				2,500	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last update
Oct.22, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

DELIVERY MODE



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the