JX040K 4A Sensitive SCR

Rev.A.1.1

DESCRIPTION:

The JX040K SCR provides high dV/dt rate with strong resistance to electromagnetic interface. It is especially recommended for use on residual current circuit breaker, straight hair, igniter etc. Package TO-252 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM} /V_{RRM}	600	V
I_{GT}	≤200	μA

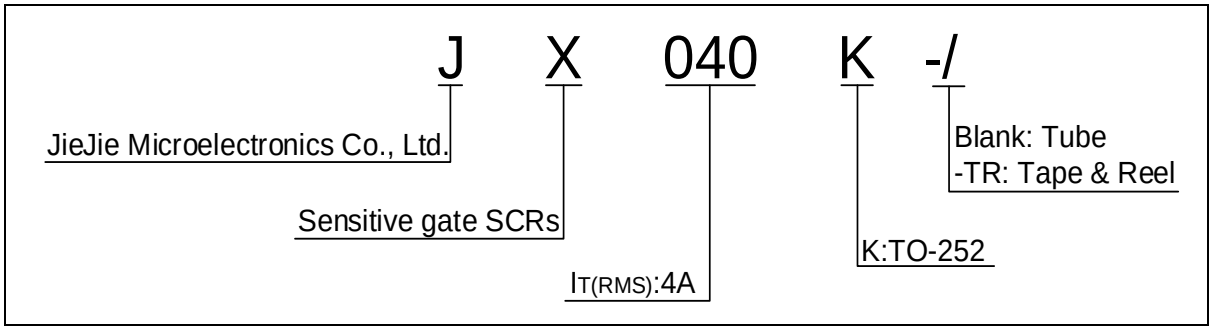
Peak gate power	P_{GM}	5	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	0.5	kV

NOTE 1: Operating junction temperature T_j is up to 125 when a resistor ≤1kΩ is connected between Gate and Cathode. Without this resistor, the T_j is up to 110 only.

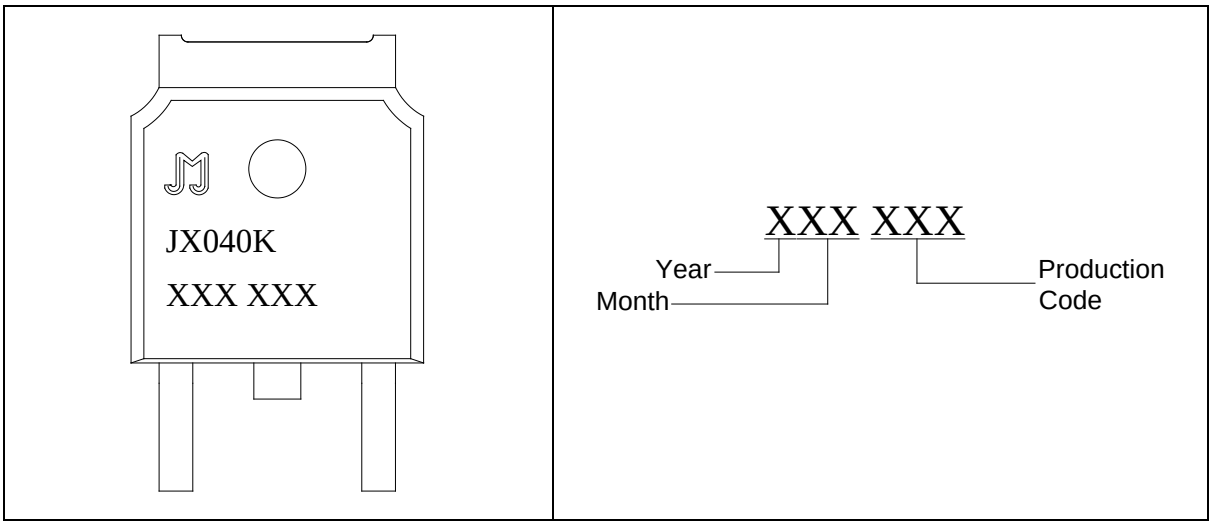
ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	V_D=12V R_L=33Ω	-	50	200	μA
V_{GT}		-	0.6	0.8	V
V_{GD}	V_D=V_{DRM} T_j=125	0.2	-	-	V
I_L	I_G=1.2 I_{GT}	-	-	6	mA
I_H	I_T=0.05				

ORDERING INFORMATION



MARKING

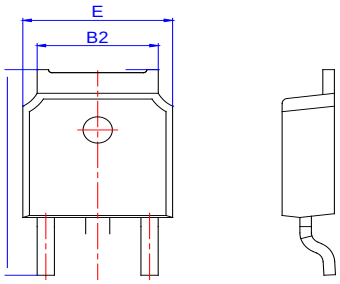


JX040K

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature (-)IGT, IL (J)/IGT, IL (F25-)

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PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065

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