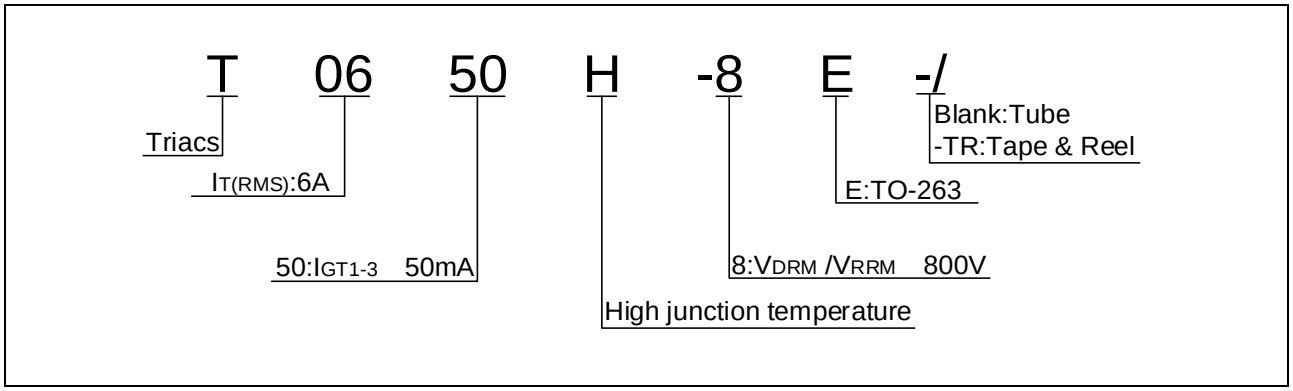

T
06
50
H
-8
E
-/
Triacs
IT(RMS):6A
50:IGT1-3 50mA
High junction temperature
8:VDRM /VRRM 800V
E:TO-263
Blank:Tube
-TR:Tape & Reel

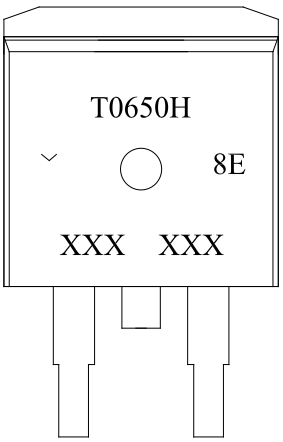

T0650H
8E
XXX XXX

FIG.1: Maximum power dissipation versus RMS on-state current

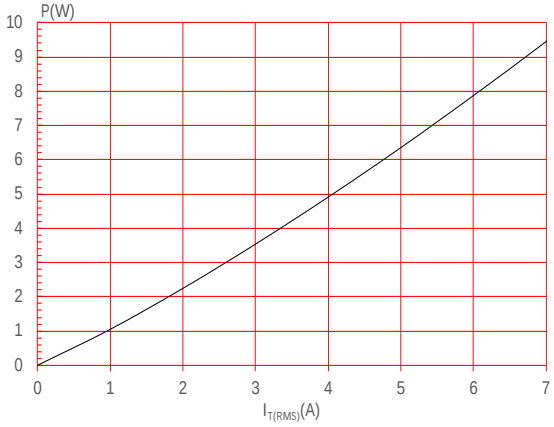


FIG.2: RMS on-state current versus case temperature

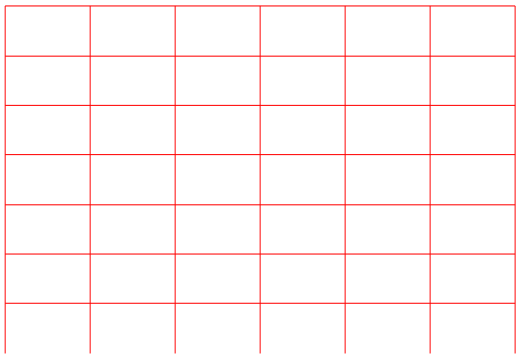
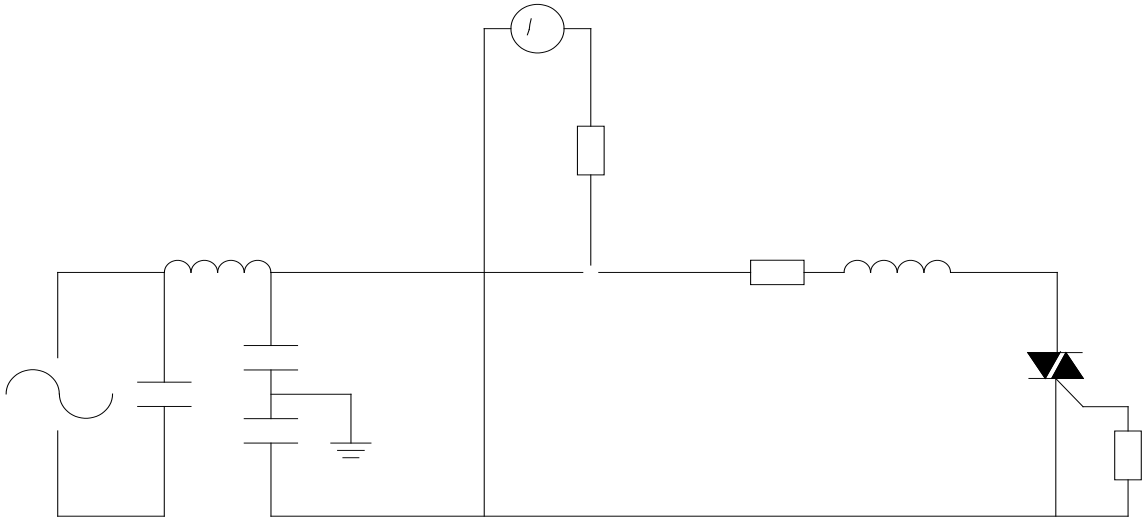
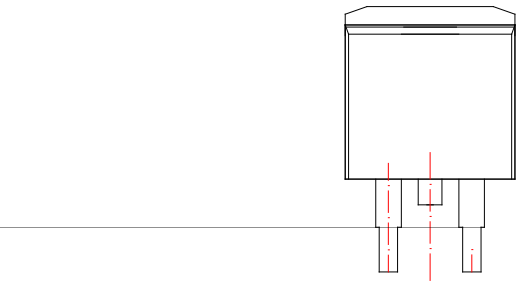


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards R oa d Model





Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.40		9.60	0.370		0.378
D	2.40			0.094		
E	1.20		1.50	0.047		0.059
F	0.75		0.85	0.029		0.033



Information furnished in this document is believed to be accurate and reliable. However,