T1610H-8F 16A TRIAC

Rev.A.1.1

DESCRIPTION:

The T1610H-8F triac i 166 ‘ i J RD ž ă Ů r

Peak gate current (t_p=20μs , T_j=150)	I_{GM}	4	A
Average gate power dissipation (T_j=150)	$P_{G(AV)}$		

ORDERING INFORMATION

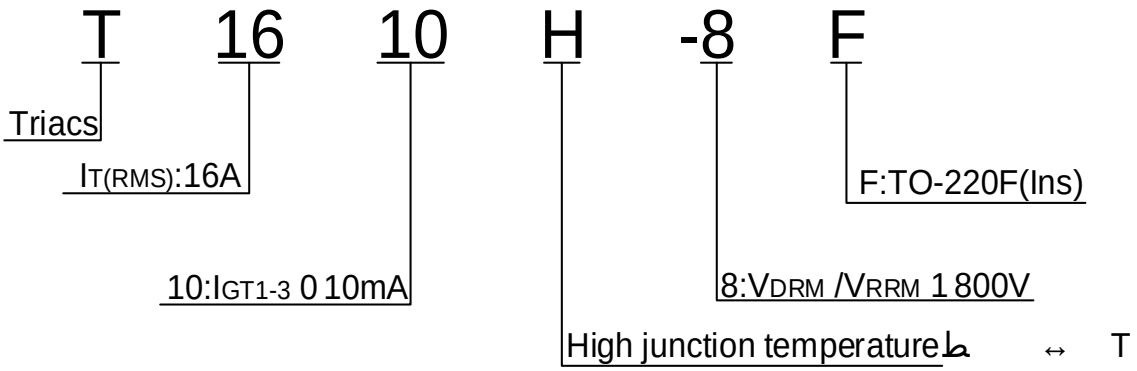


FIG.1: Maximum power dissipation versus RMS on-state current

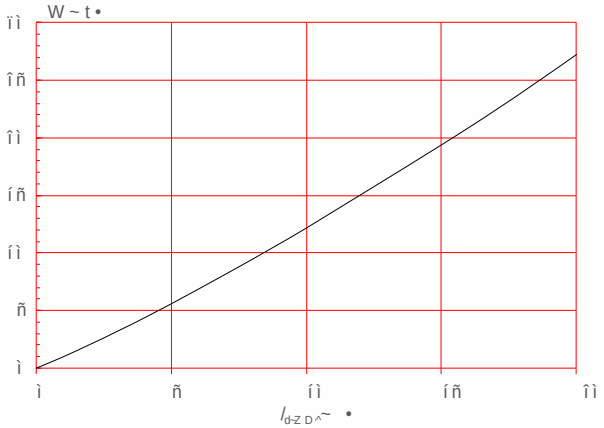


FIG.2: RMS on-state current versus case temperature

FIG.3: Surge peak on-state current versus number of cycles

FIG.4: On-state characteristics

FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



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