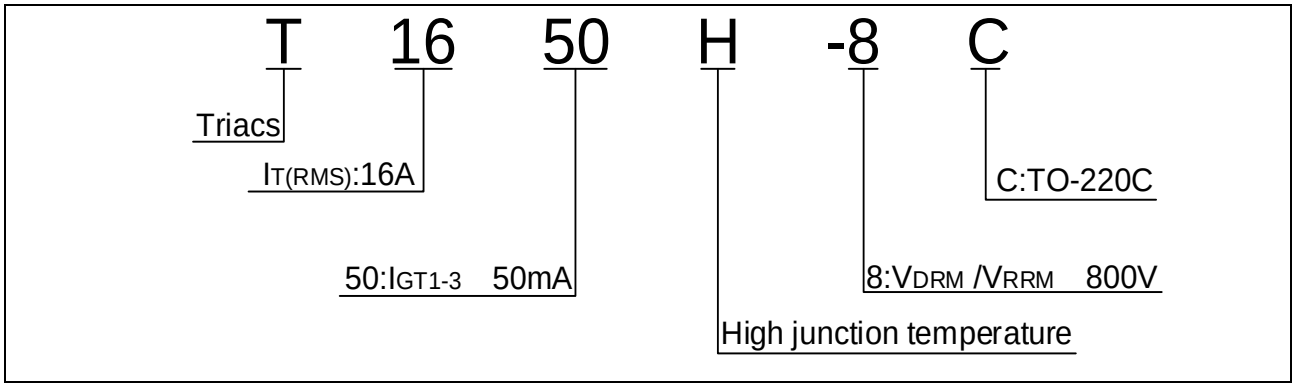
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =150 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	80	mA
				100	
I_H	I_T =500mA		MAX.	60	mA
dV/dt	V_D=540V Gate Open T_j =150		MIN.	1800	V/μs
(dI/dt)c	(dV/dt)c=20V/μs, T_j=150		MIN.	25	A/ms

ORDERING INFORMATION



MARKING

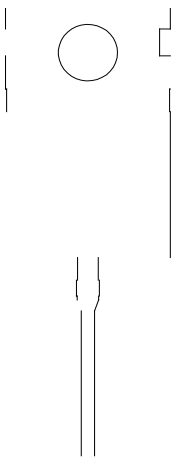


FIG.1: Maximum power dissipation versus RMS on-state current

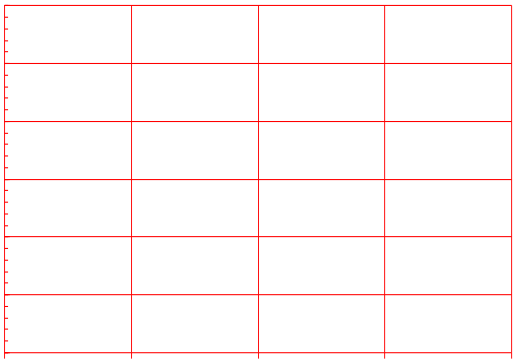
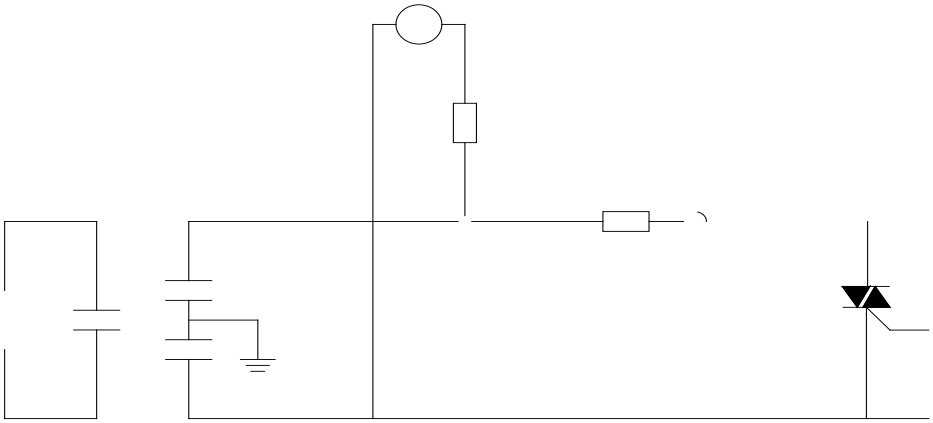


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T1650H-8C	800	50	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Oct.14, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

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