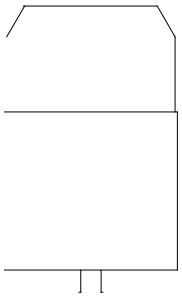


Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	1.2	

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ORDERING INFORMATION

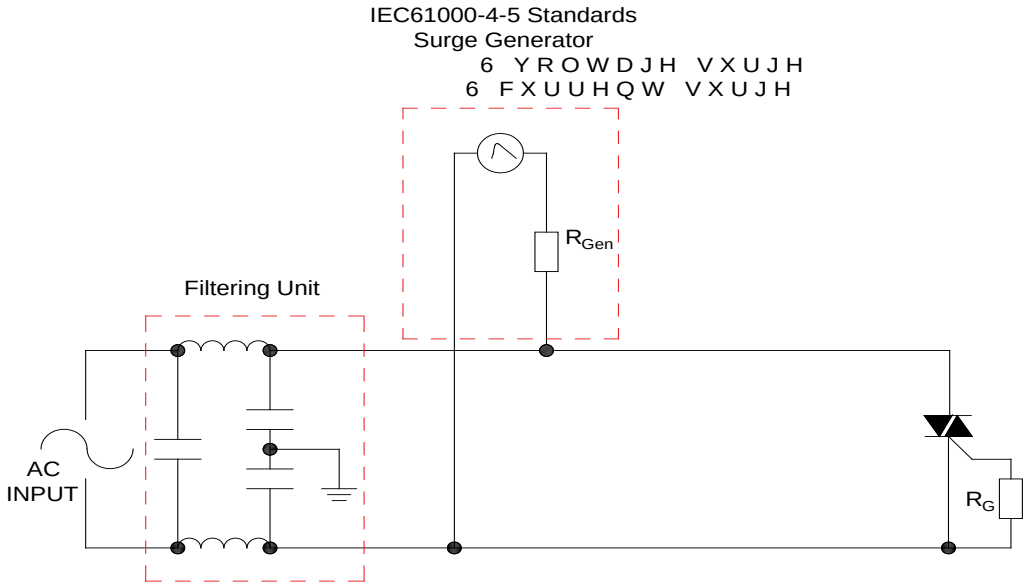
MARKING



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FIG.1 Maximum power dissipation versus RMS on-state current

FIG.7 ÖTest circuit for inductive and resistive loads according to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to

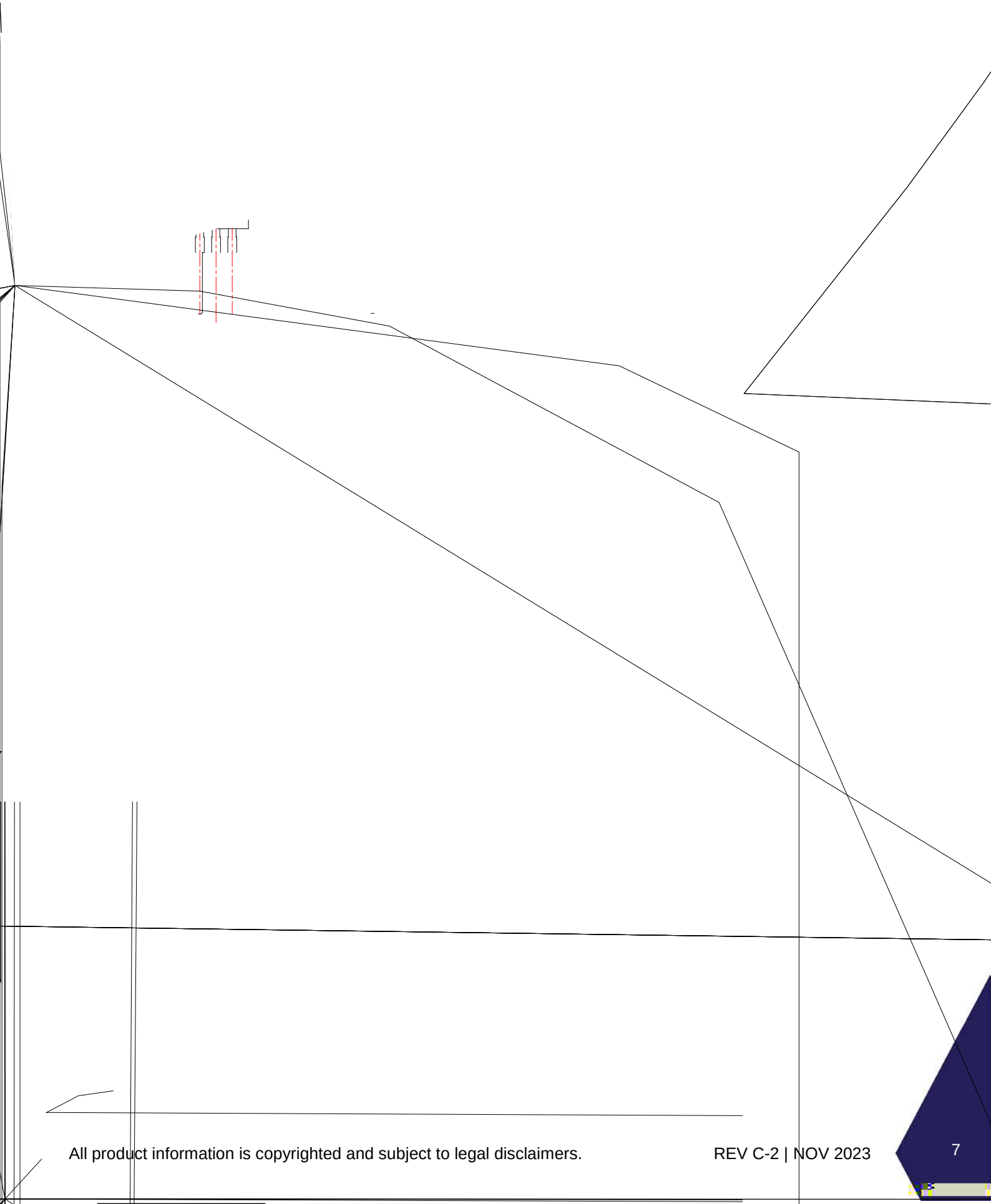
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ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T3050H-6B-US	600	50	TO-220B	50	Tube

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PACKAGE MECHANICAL DATA



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