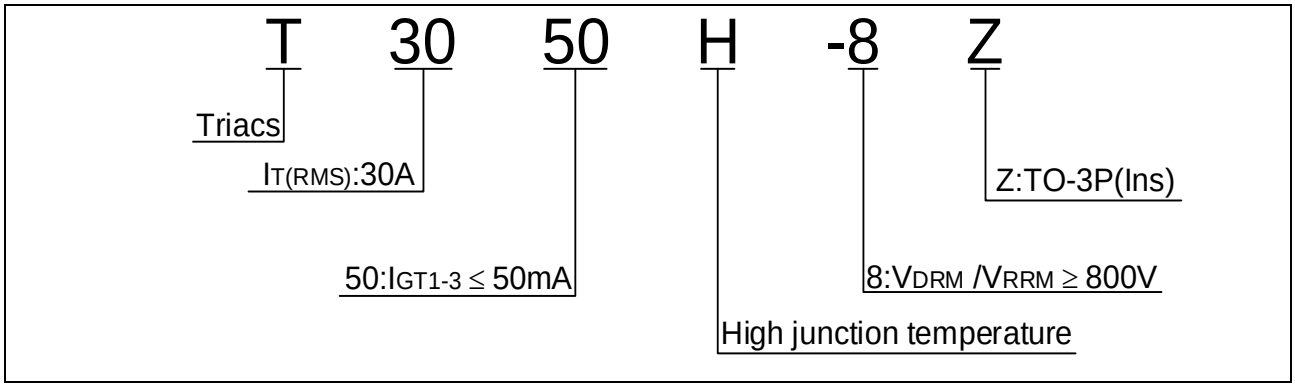
Avere gate pow (6 (er))13 dissipati	on (Ta=25 |)	$P_{G(AV}$	1	W


T
30
50
H
-8
Z
Triacs
IT(RMS):30A
50:IGT1-3 ≤ 50mA
High junction temperature
8:VDRM /VRRM ≥ 800V
Z:TO-3P(Ins)

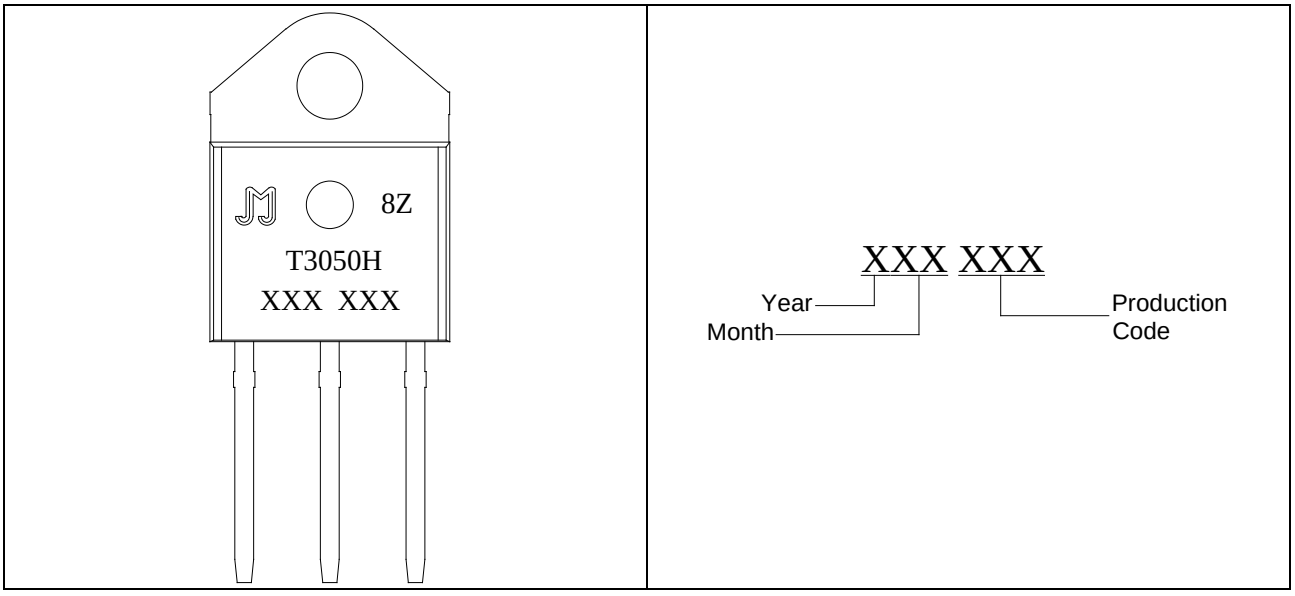

8Z
T3050H
XXX XXX
XXX XXX
Year
Month
Production
Code

FIG 1: Maximum power dissipation versus RMS on-state current

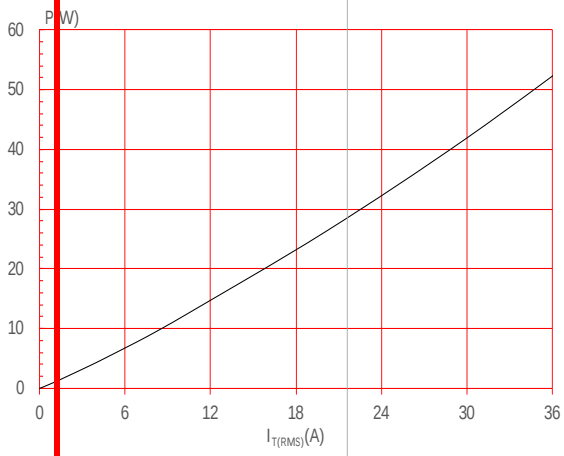


FIG.2: RMS on-state current versus case temperature

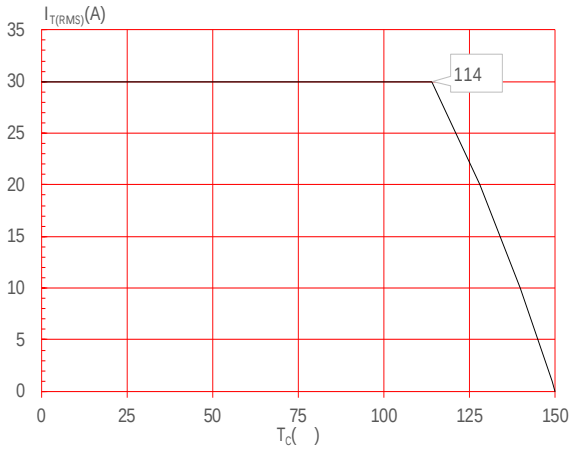


FIG 3: Surge peak on-state current versus number of cycles

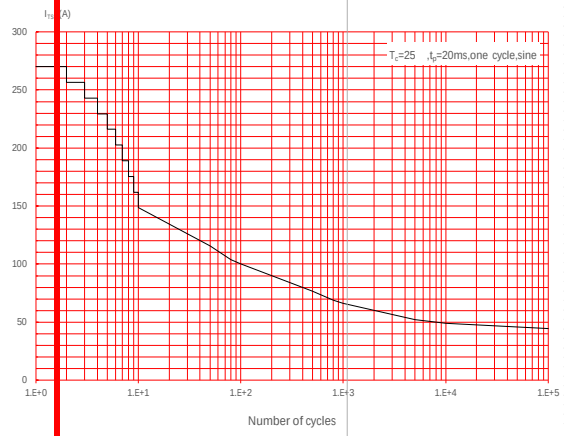


FIG.4: On-state characteristics

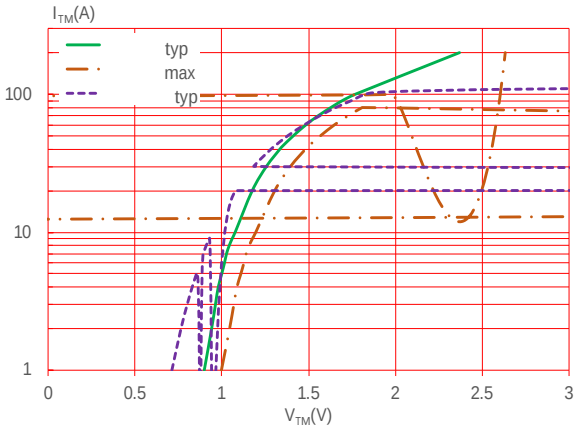
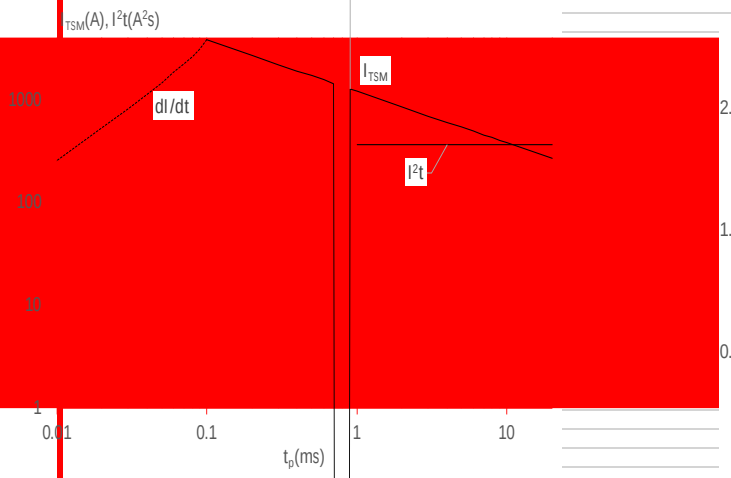


FIG 5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width t_p

2



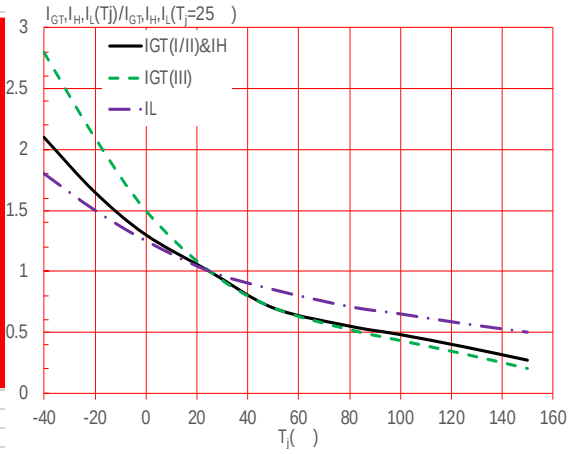
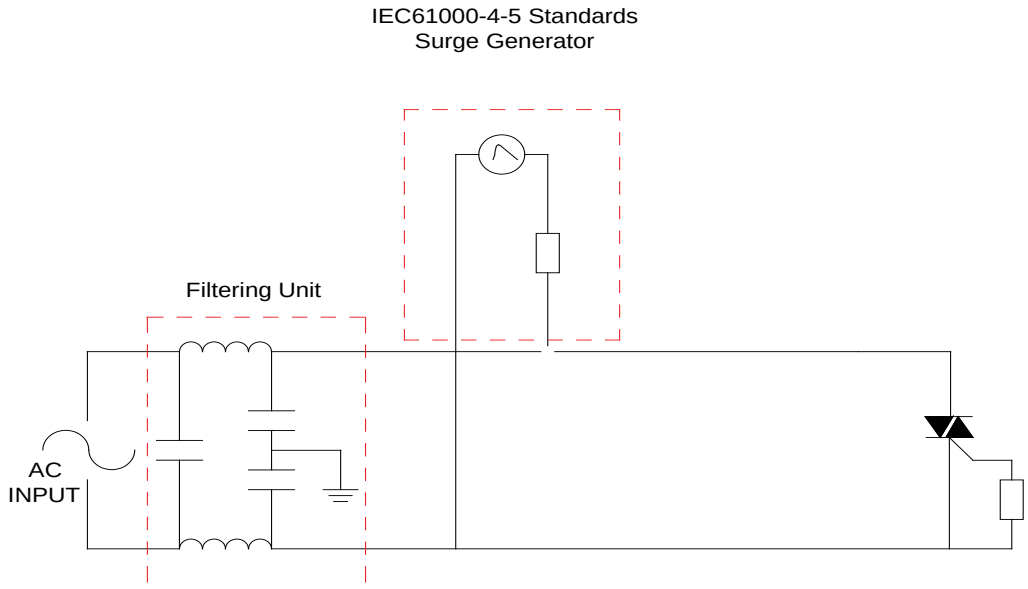


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
T3050H-8Z	800	50	TO-3P(Ins)	30	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.16, 2025		



Information furnished in this document is believed to be accurate and reliable. However,