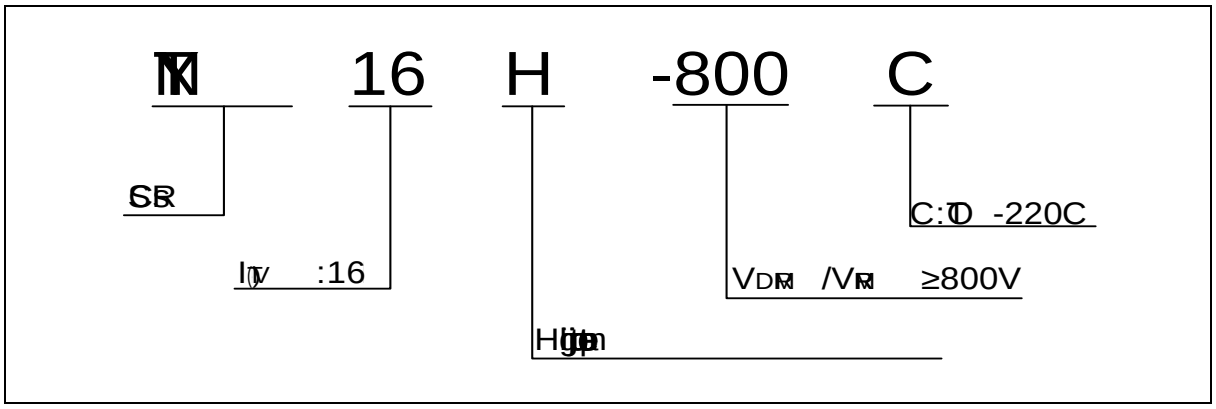
[illegible]	(t_p=20μs, T_j=150)	I_{GM}	5	
[illegible]	(T_j=150)	$P_{G(AV)}$	1	W
[illegible]		P_{GM}	20	W

[illegible]
(T_j=25 ; [illegible] -

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MARKING

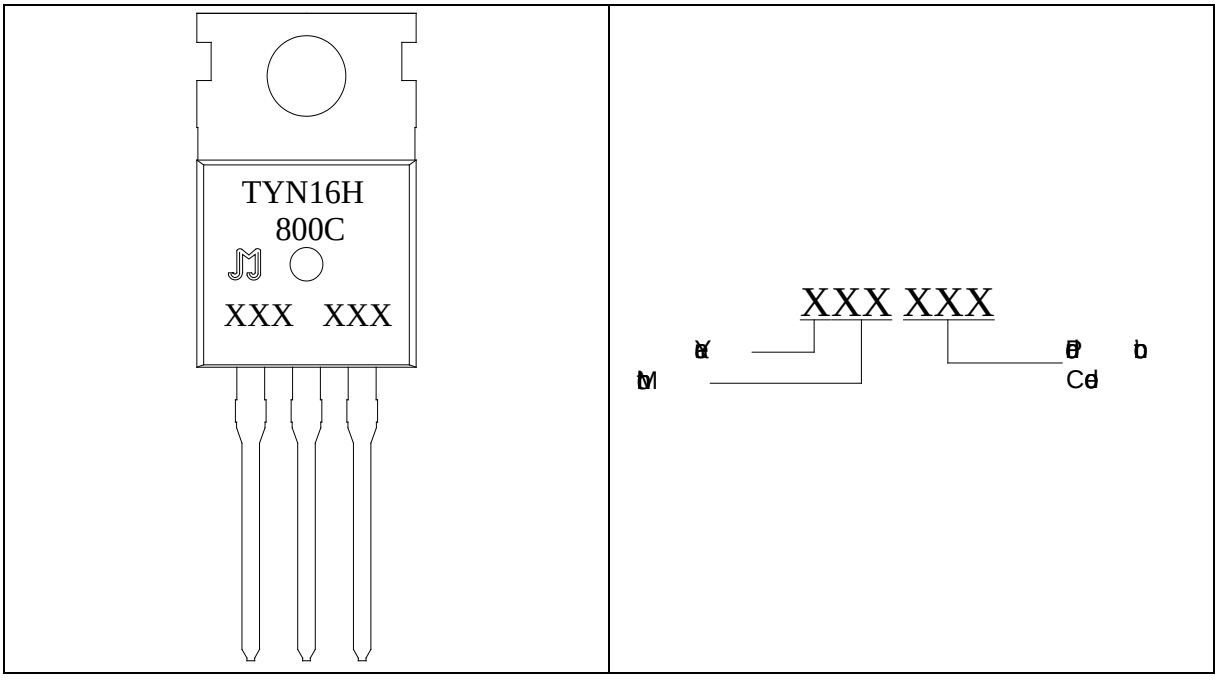


FIG.1: Maximum RMS on-state power dissipation versus RMS on-state current

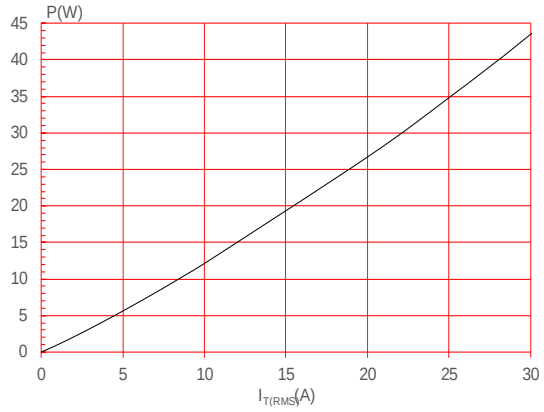
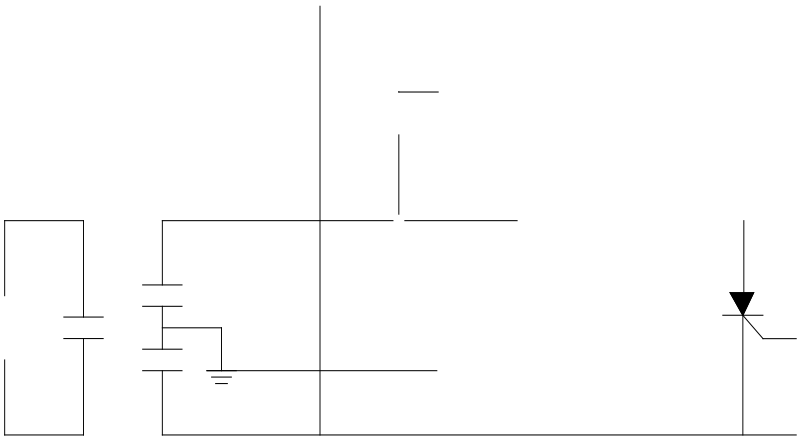


FIG.2: RMS on-state current versus case temperature



FIG.7 Test circuit for IEC61000-4-5 standard



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Ord	Vg V_{DRM}/V_{RRM} (V)	IGT(A)	Pg	Be (p	Dy ch
TYN16H-800C	800	15	TO-220C	50	Tu

Dn Ri Hy

Da	R	Cg
p .13, 2023	. 1.0	t
O .14, 2025	. 1.1	Ri PKGE MCHNCL DT

L3 0.173

0.028

0.018

L2 0.049 0.067 0.094 0.045

C2

0.272

10.30 0.390

0.087 0.102

0.248

6.30 6.90

0.116

9.90

2.40

8.90