JST02W-800SW 2A TRIAC

Rev.A.1.1

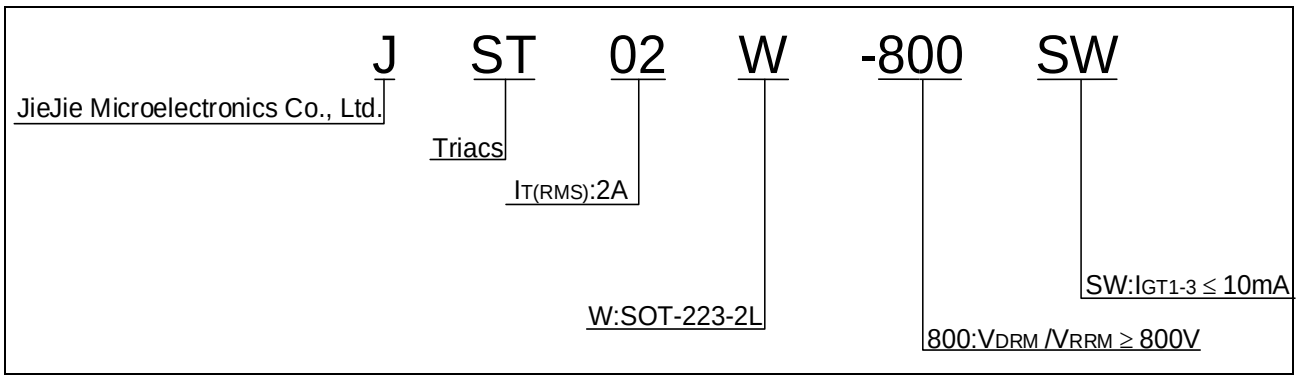
DESCRIPTION:

Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	20	mA
				30	
I_H	I_T =100mA		MAX.	15	mA

ORDERING INFORMATION



MARKING

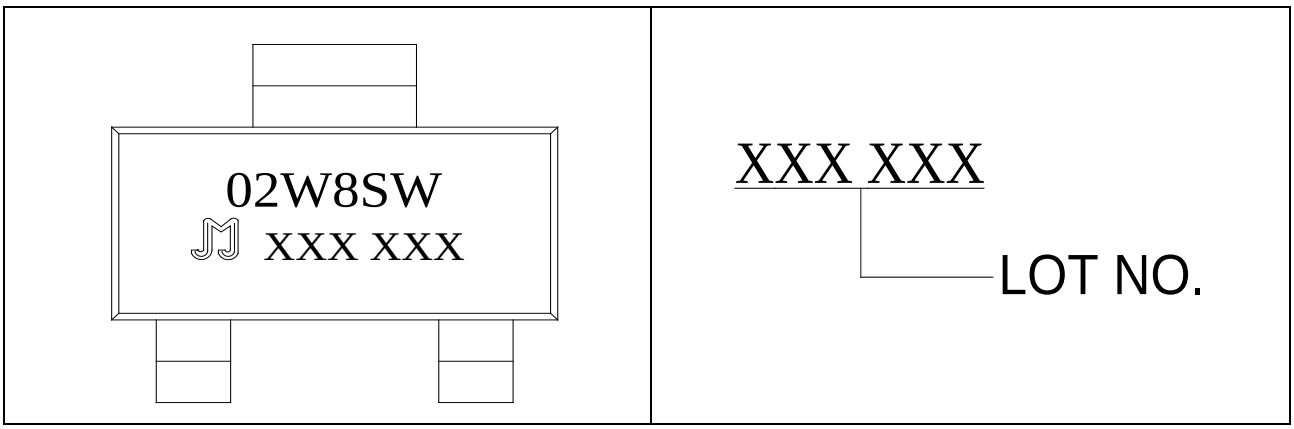


FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST02W-800SW	800	10	SOT-223-2L	4,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.24, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

JST0

PACKA



TEL +86

DELIVERY MODE

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	-		12.30	-		0.482
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219 (2
D0		1.55	1.60		0.21482	
D1		-				
P0	3.90	4.00	4.10			
P1	7.90	8.00	8.10			
P2	1.95	2.00	2.05			
10P0	39.80	40.00	40.20			
A0	6.85	6.95	7.05			
B0	7.15	7.25	7.35			
K0	1.95	2.05	2.15			
T	0.20	0.25	0.30			