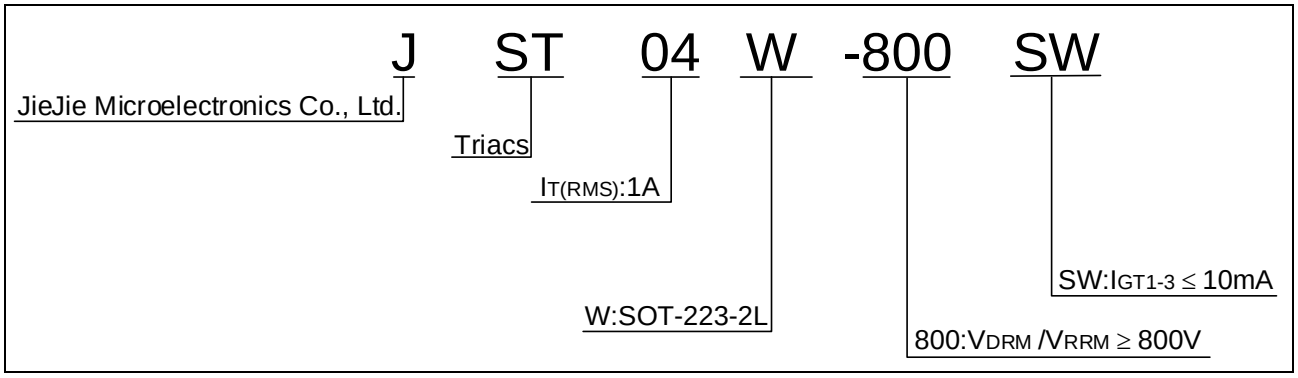
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	3	kV

T364BT /P 48 0.40.48 re090.2 753.14BT /P 48 0.40.48 re09

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol Test Condition Quadrant Quadr o-5Tw uo-567 753.48 Td ( )Tj 2MC /P <</MCID 1

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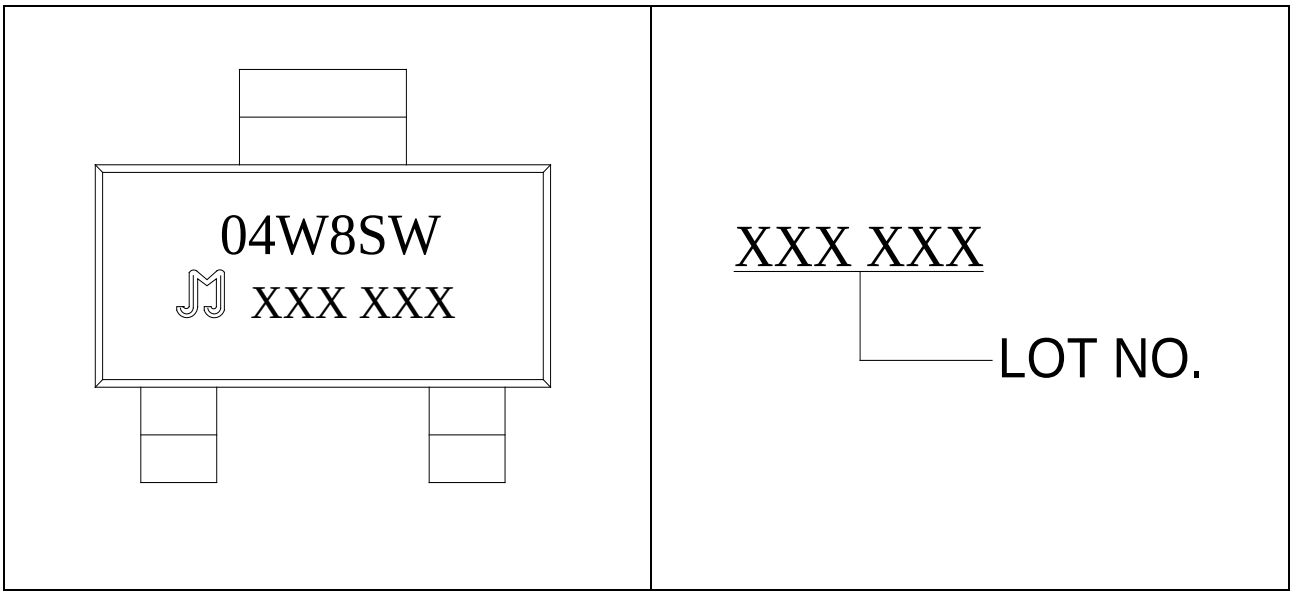


FIG.1: Maximum power dissipation versus RMS on-state current

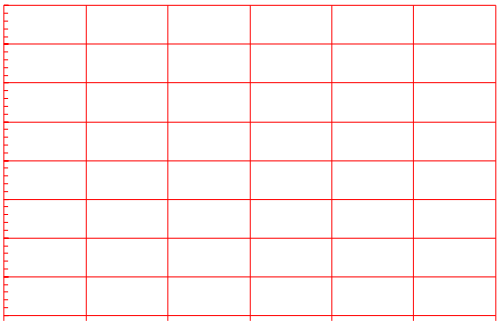
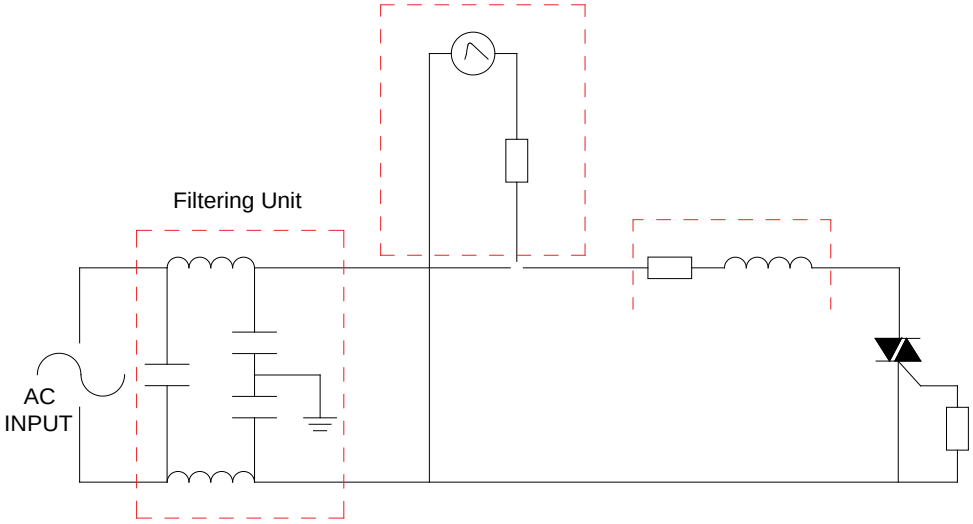


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

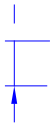
IEC61000-4-5 Standards

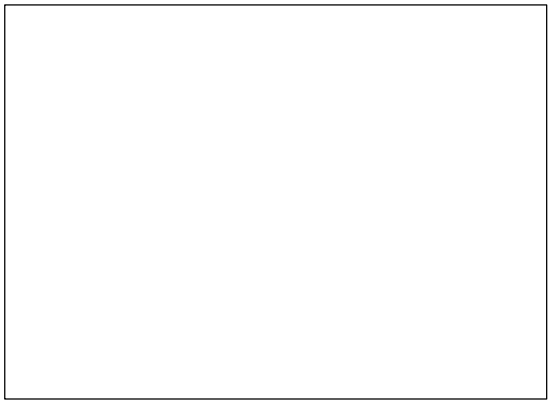


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PACKAGE MECHANICAL DATA





DELIVERY MODE

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W						
E						
F						
D0						
D1D0 F .						

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