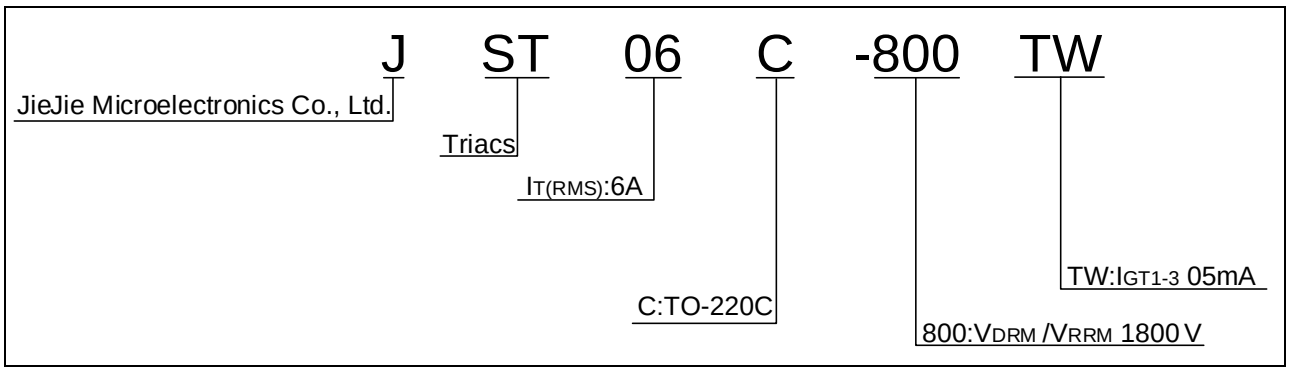
| | | | |
|---|---|---|---|
| Peak gate power | $P_{GM}$ | 10 | W |
| Peak pulse voltage | | | |

## ORDERING INFORMATION




## MARKING

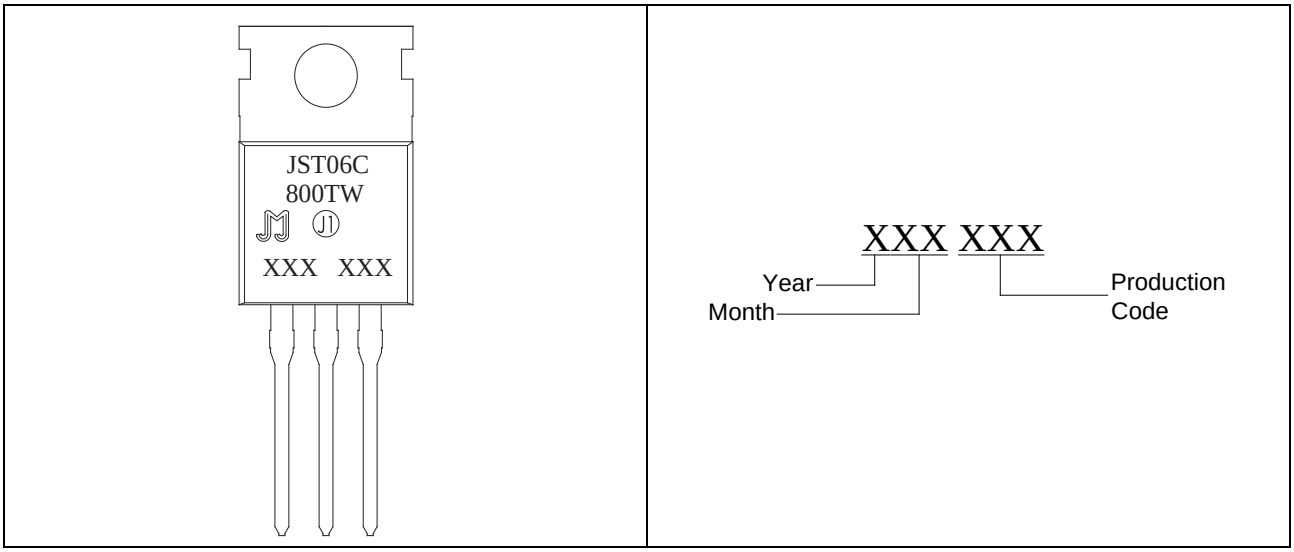


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



## ORDERING INFORMATION

| Order code | Voltage $V_{DRM}/V_{RRM}$ (V) |
|---|---|

PACKAGE MECHANICAL DATA

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