JST08H-1200CW 8A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST08H-1200CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST08H-1200CW snubberless triac is especially recommended for use on inductive loads. From T2 terminals to external heatsink. Package TO-251 is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM} /V_{RRM}	1200	V
I_{GT} / /	35/35/35	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
RMS on-state current ($T_C \leq$ 91)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)		88	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	32	A^2s
Critical rate of rise of on-state current (I_G=2 I_{GT} , f=100Hz , T_j=125)	dI/dt	100	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	4	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	2	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	50	mA
				60	
I_H	I_T =100mA		MAX.	35	mA
dV/dt	V_D=800V Gate Open T_j =125		MIN.	300	V/μs
(dI/dt)c	(dV/dt)c=20V/μs, T_j=125		MIN.	10	A/ms
t_{on}	I_G=40mA I_A=200mA I_R=20mA T_j=25		TYP.	3	μs
t_{off}				30	

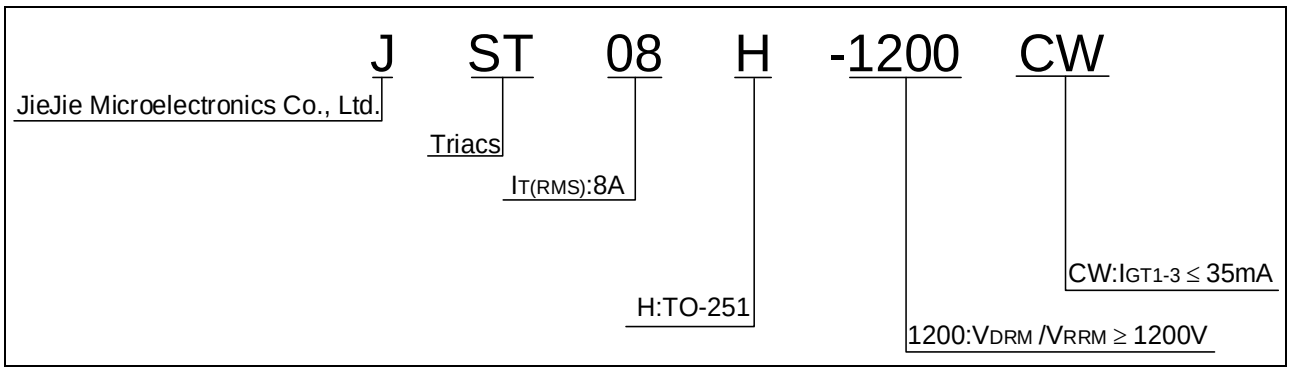
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =11A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.82	V
R_D	Dynamic resistance	T_j=125	45	mΩ
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	10	μA
I_{RRM}		T_j=125	1	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	3	/W
$R_{th(j-a)}$	junction to ambient (AC)	100	/W

ORDERING INFORMATION



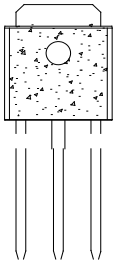
MARKING

FIG.1: Maximum powax (u)11.sp125p7.1tMJ 0 8()-8-6.9 Tc -0.009 56.6-3.10W n .466 55.2onw 0.829 0 Td [Tc

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			

PACKAGE MECHANICAL DATA



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.