JST08K-1200B W 8A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST08K-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST08K-1200BW snubberless triac is especially recommended for use on inductive loads. Package TO-252 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM} /V_{RRM}	1200	V
I_{GT} / /	50/50/50	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
RMS on-state current (T_C 091)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)		88	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	32	A^2s
Critical rate of rise of on-state current (I_G=2× I_{GT} , f=100Hz , T_j=125)	dI/dt	100	A s
Peak gate current (t_p=20 s , T_j=125)	I_{GM}	4	A
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

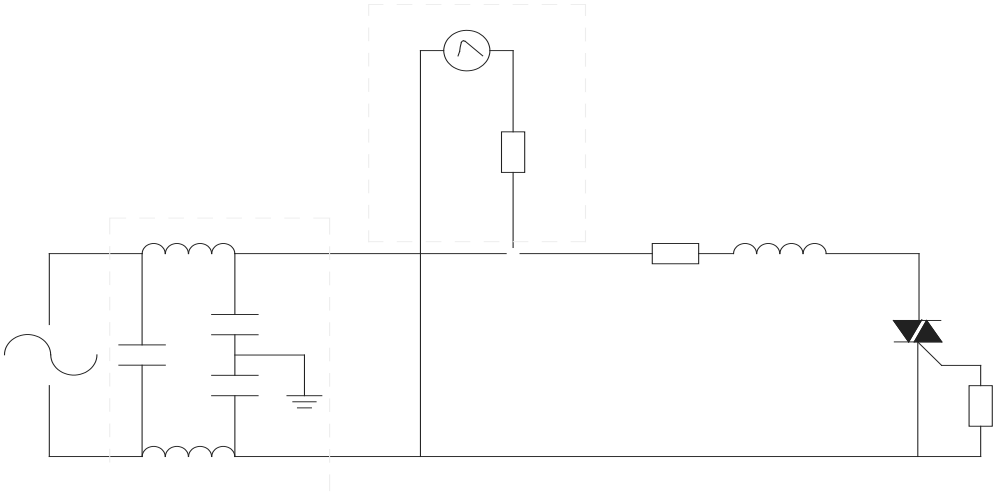
ORDERING INFORMATION

J ST 08 K -1200 BW -/

Jie0oBT /T-0.028 Tl 1 Tf -0.028 TctI0oBT /Tn0.028 T3 1 Tf 24 C 1 Tf o 0.007 Tc 1 w61

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



PACKAGE MECHANICAL DATA

