JST12E-600TW **Jė Jė M**

FIG.1: Maximum power dissipation versus RMS on-state current

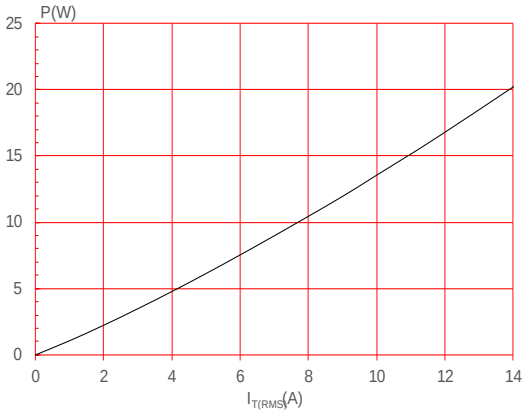


FIG.2: RMS on-state current versus case temperature

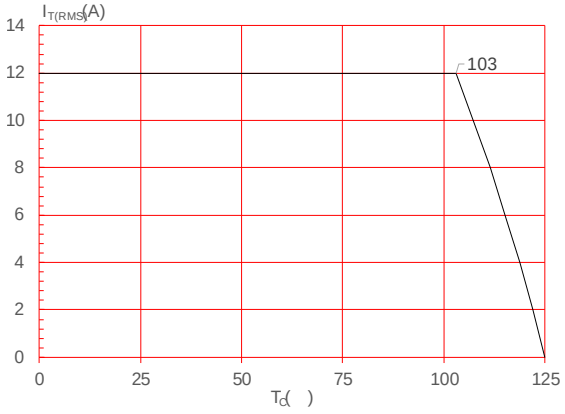


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

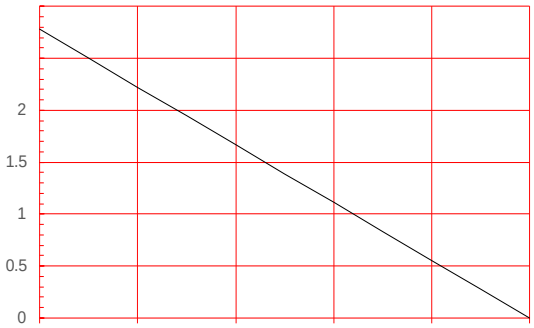


FIG.4: Surge peak on-state current versus number of cycles

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

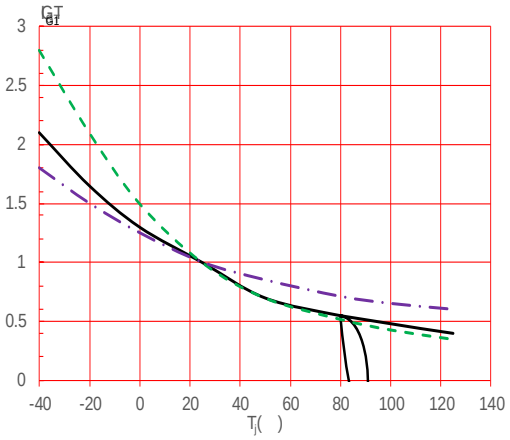
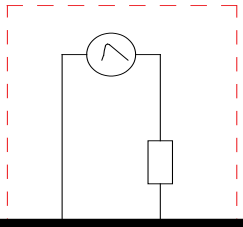


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



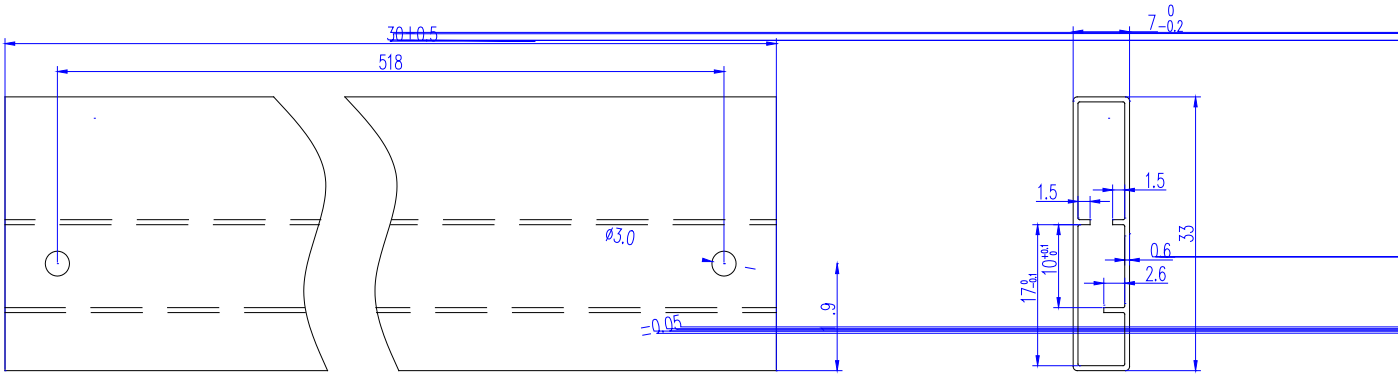
ORDERING INFORMATION

Ordercode	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty (pcs)	Delivery mode
		H, I- J			
JST12E-600TW	600	5	TO-263	50	Tube
JST12E-600TW-TR				800	Tape & Reel

[redacted]

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Oct.20, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

DELIVERY MODE



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