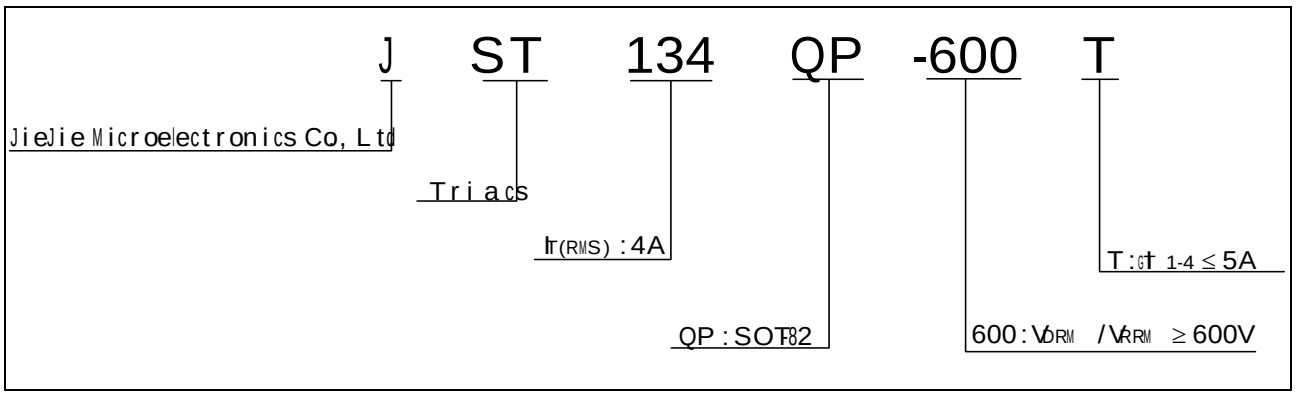
ORDERING INFORMATION



MARKING

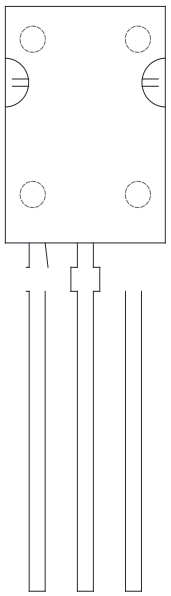


FIG.1: Maximum power dissipation versus RMS on-state current

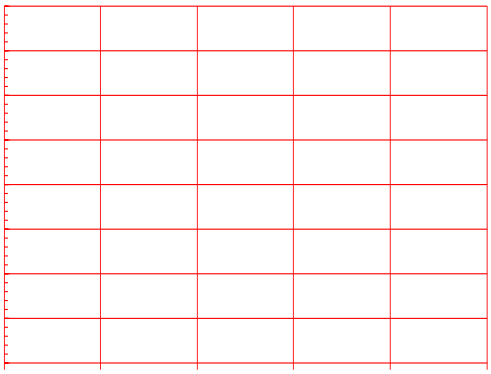
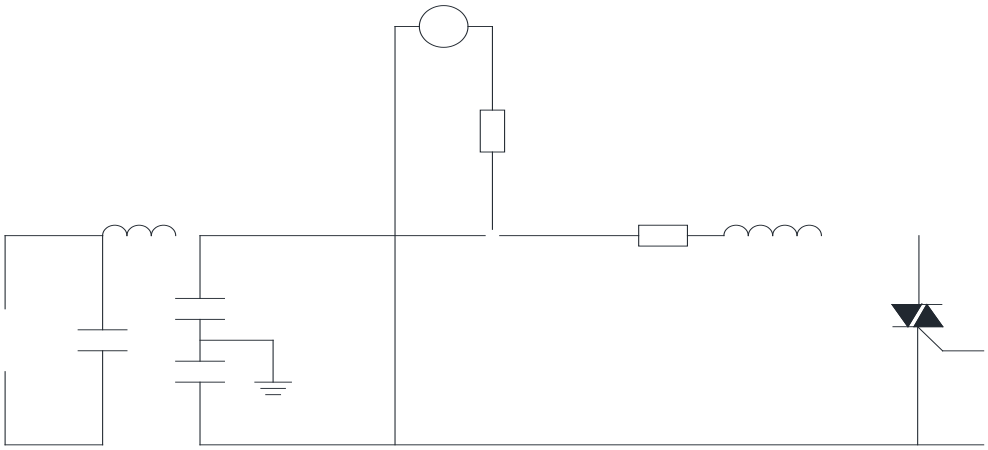


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC1000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J- K			
JST134QP-600T	**600**	**5**	**SOT-82**	**500**	**Bulk Pack**

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A1.0	Last updated

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