JST134U-800T 1A TRIAC **Rev.A.2.1**

DESCRIPTION:

The JST134U-800T triac is suitable for g[redacted]purpose AC switching. It can be used as an ON/[redacted]ction in applications such as heating regulatio[redacted]ction motor starting circuits, for phase control [redacted]on in light dimmers, motor speed controllers. [redacted]e TO-92 is RoHS compliant.

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	[redacted]e	Unit
Storage junction temperature range	T_{stg}	[redacted]50	
Operating junction temperature range	T_j	[redacted]25	
Repetitive peak off-state voltage (T_j=25[redacted]	V_{DRM}	[redacted]	V
Repetitive peak reverse voltage (T_j=25[redacted]	V_{RRM}	[redacted]	V
RMS on-state current ($T_C \leq 55$)	$I_{T(RMS)}$	[redacted]	A
Non repetitive surge peak on-state cur[redacted] (full cycle , t_p=20ms , T_j=25)	I_{TSM}	[redacted]	A
Non repetitive surge peak on-state cur[redacted] (full cycle , t_p=16.6ms , T_j=25)		[redacted]	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	[redacted]5	A^2s
Critical rate of rise of on-state current (I_G≠2 × I_{GT} , / [redacted] Tm ┐			

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	ALL	MAX.	5	mA
V_{GT}		ALL	MAX.	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	- -	MAX.	9	mA
				13	
I_H	I_T=100mA		MAX.	5	mA
dV/dt	V_D=540V Gate Open T_j=110		MIN.	20	V/μs
(dV/dt)c	(dI/dt)c=1.8A/ms, T_j=110		MIN.	1.2	V/μs
t_{on}	I_G=10mA I_A=200mA I_R=20mA T_j=25		TYP.	2	μs
t_{off}				20	

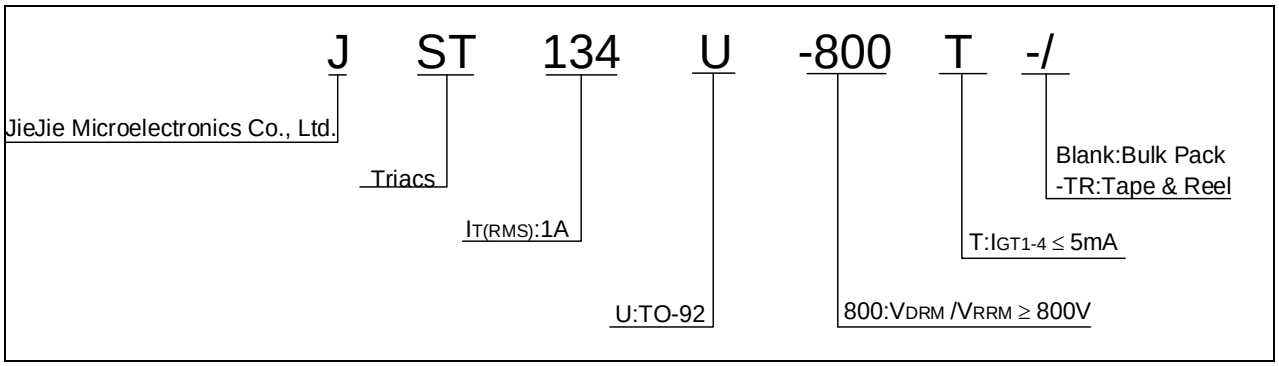
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM}=5A t_p=380μs	T_j=25	1.55	V
V_{TO}	Threshold voltage	T_j=125	0.92	V
R_D	Dynamic resistance	T_j=125	107	mΩ
I_{DRM}	V_D=V_{DRM} V_R=V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	0.35	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	60	/W
$R_{th(j-a)}$	junction to ambient (AC)	150	/W

ORDERING INFORMATION



MARKING

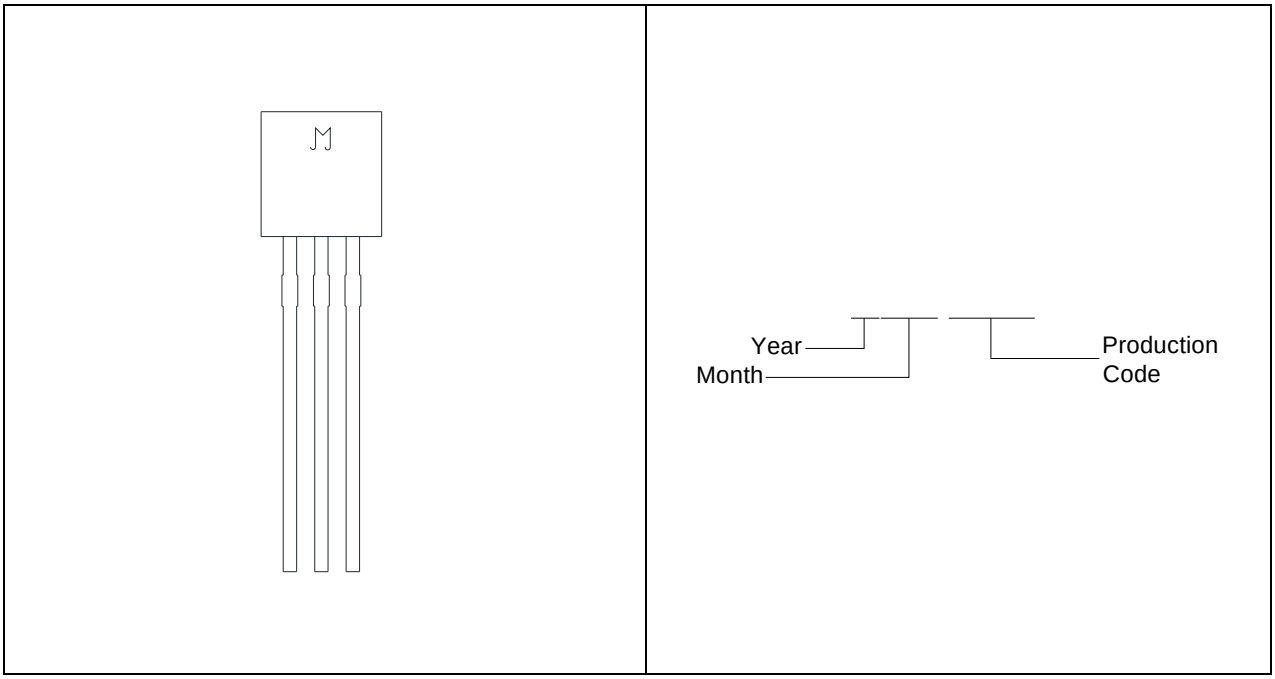
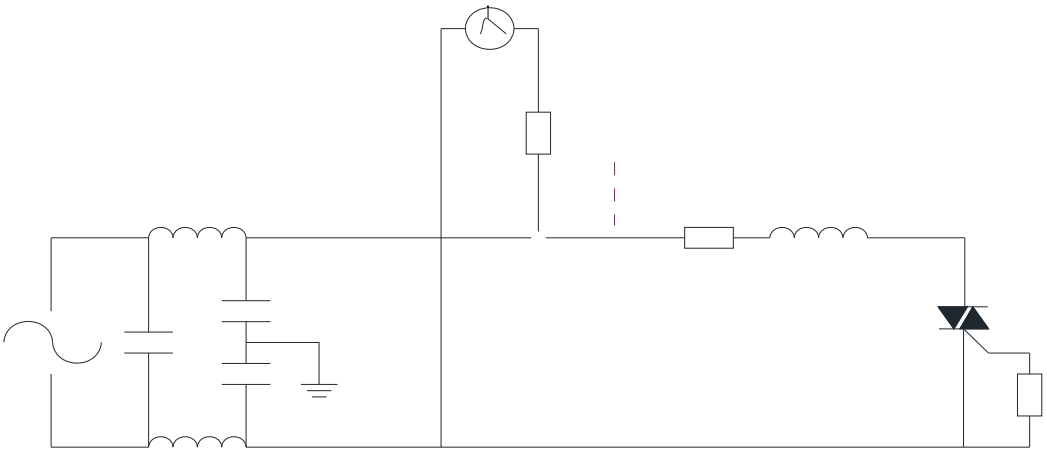


FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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JST134U-800T	800	5	TO-92	1,000	Bulk Pack
JST134U-800T-TR				2,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Mar.31, 2025	A.2.0	Renew PACKAGE MECHANICAL DATA
Sept.28, 2025	A.2.1	Revise PACKAGE MECHANICAL DATA

DELIVERY MODE

PACKAGE	

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