Peak pulse voltage
(T_j=25 ; non-repetitive,off-

FIG.1: Maximum power dissipation versus RMS on-state current

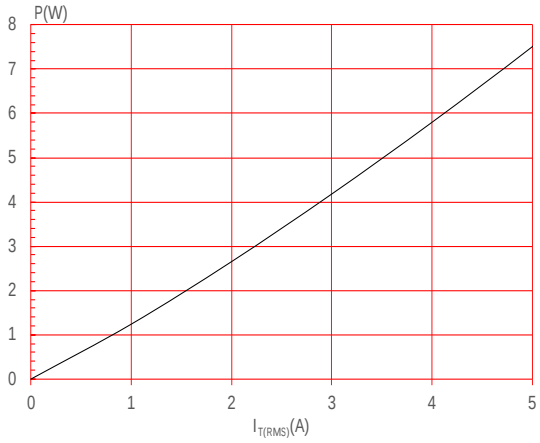


FIG.2: RMS on-state current versus case temperature

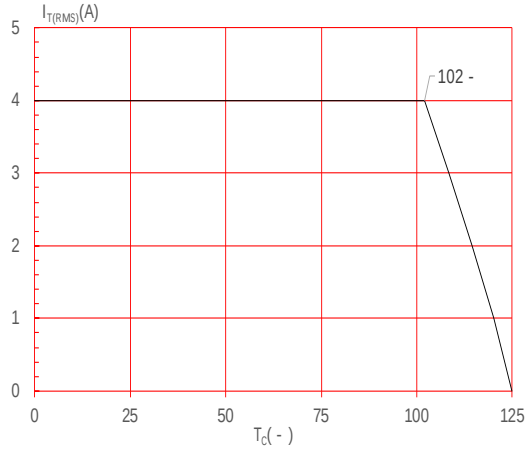
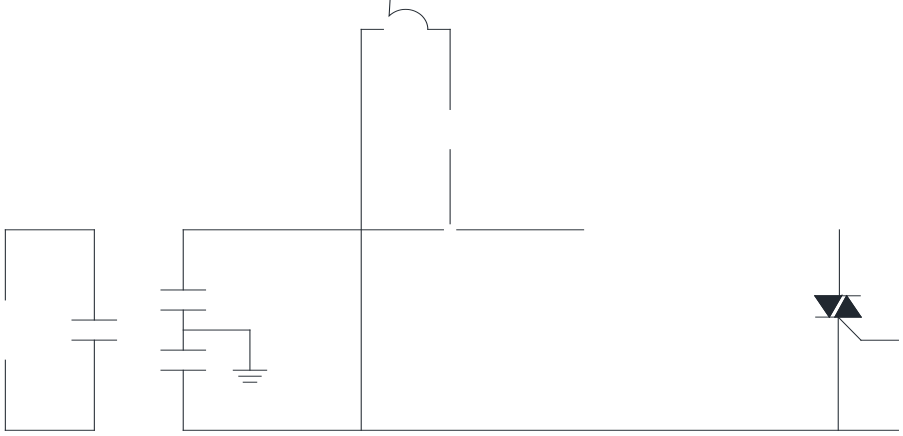


FIG.3: Surge peak on-state current versus number of cycles

FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	

513-68527.4 (pi4 (l)ve.6 0.

JST136A-600F