JST136C-800T 4A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST136C-800T triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. From T2 terminals to external heatsink. Package TO-220C is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM} /V_{RRM}	800	V
I_{GT} / / /	5/5/5/5	mA

ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T_{stg}	-40-150	
Operating junction temperature range		T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)		V_{DRM}	800	V
Repetitive peak reverse voltage (T_j=25)		V_{RRM}	800	V
RMS on-state current (T_C 110)		$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)		I_{TSM}	40	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)			44	
I^2t value for fusing (t_p=10ms , T_j=25)		I^2t	8	A^2s
Critical rate of rise of on-state current (I_G=2×I_{GT} , f=100Hz , T_j=125)	-	dI/dt	50	A s
	-		30	
Peak gate current (t_p=20 s , T_j=125)		I_{GM}	2	A
Average gate power dissipation (T_j=125)		$P_{G(AV)}$	0.5	W
Peak gate power		P_{GM}	5	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)		V_{pp}	2.5	kV

ORDERING INFORMATION

J ST 136 C -800 T

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FIG.1: Maximum power dissipation versus RMS on-state current

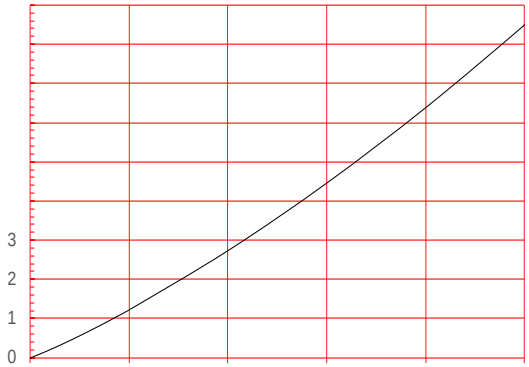
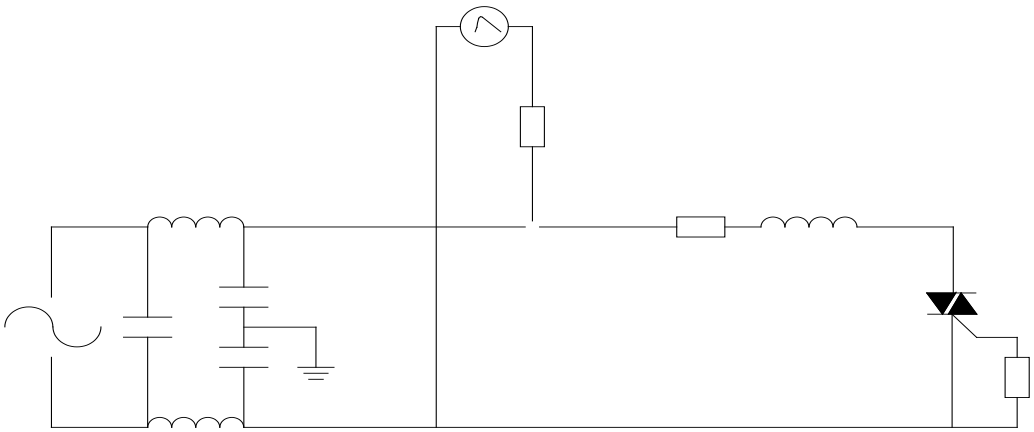


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- - -			
JST136C-800T	800	5	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



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