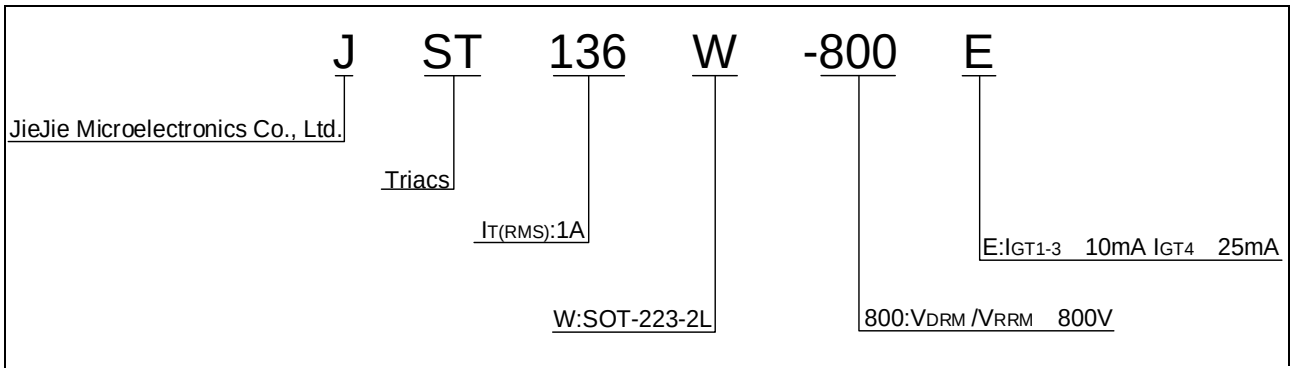
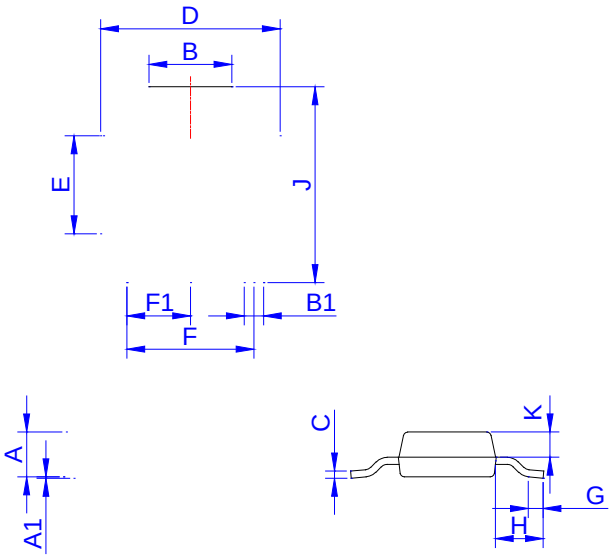
(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	- -	MAX.	10	mA
				25	
V_{GT}		ALL	MAX.	1.3	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	-	MAX.	20	mA
		-		30	
I_H	I_T=100mA		MAX.	15	mA
dV/dt	V_D=540V Gate Open T_j=110		MIN.	250	V/μs
(dV/dt)c	(dI/dt)c=1.8A/ms, T_j=110				


J
ST
136
W
-800
E
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):1A
W:SOT-223-2L
800:VDRM /VRRM 800V
E:IGT1-3 10mA IGT4 25mA


D
B
E
J
F1
B1
F
A
A1
C
K
G
H

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	-		12.30	-		0.482
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219
D0		1.55	1.60		0.061	0.063
D1		-	-			
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.95	2.00	2.05	0.077	0.079	0.081
10P0	39.80	40.00	40.20	1.567	1.575	1.583
A0	6.85	6.95	7.05	0.269	0.273	0.276
B0	7.15	7.25	7.35	0.280	0.284	0.288
K0	1.95	2.05	2.15	0.076	0.080	0.084
T	0.20	0.25	0.30		0.010	0.012

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