	Symbol	Test Condition	Quadrant	Value	Unit	

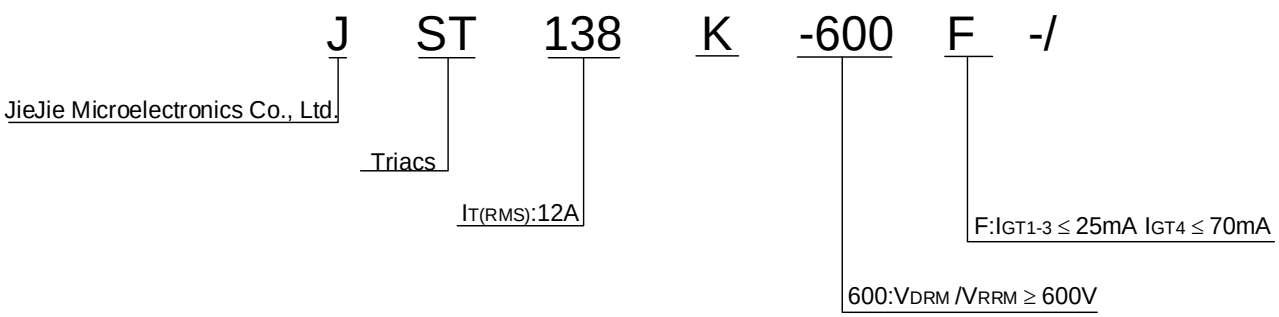

J ST 138 K -600 F -/
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):12A
600:VDRM /VRRM ≥ 600V
F:IGT1-3 ≤ 25mA IGT4 ≤ 70mA

FIG.1: Maximum power dissipation versus RMS on-state current

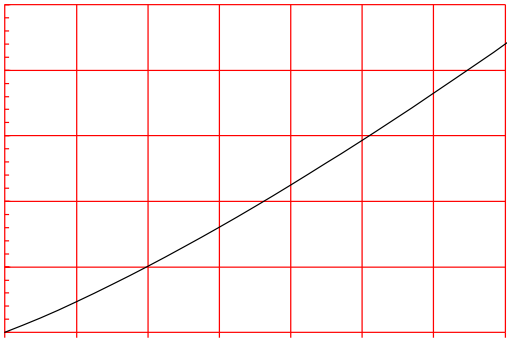


FIG.2: RMS on-state current versus case temperature

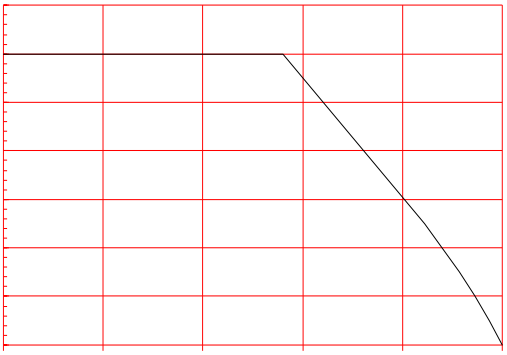


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

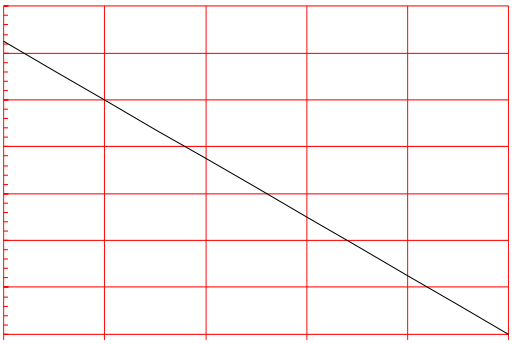
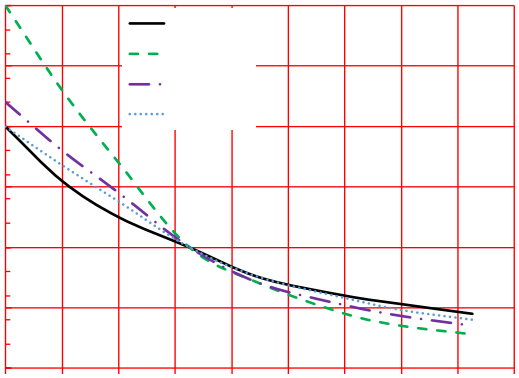


FIG.4: Surge peak on-state current versus number of cycles

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

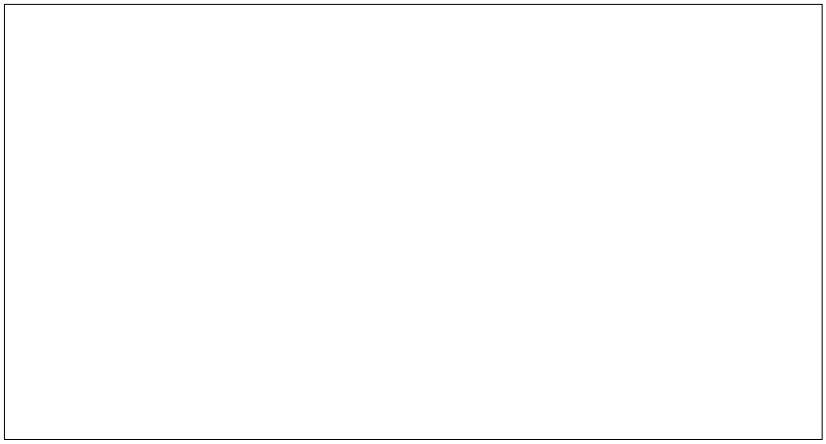


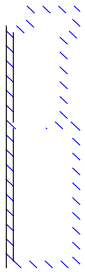
Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST138K-600F	600	25	70	TO-252	80	Tube
JST138K-600F-TR					2,500	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.22, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

Dimen 3m8> 372 272-6 (CA5e)-(





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