DESCRIPTION:

The JST16A-800Sac is suitable for general purpose AC ， switching. It can be used as an ON/OFF fun
recommended for

ABSOLUTE MAXIMUM RATINGS

Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

ORDERING INFORMATION

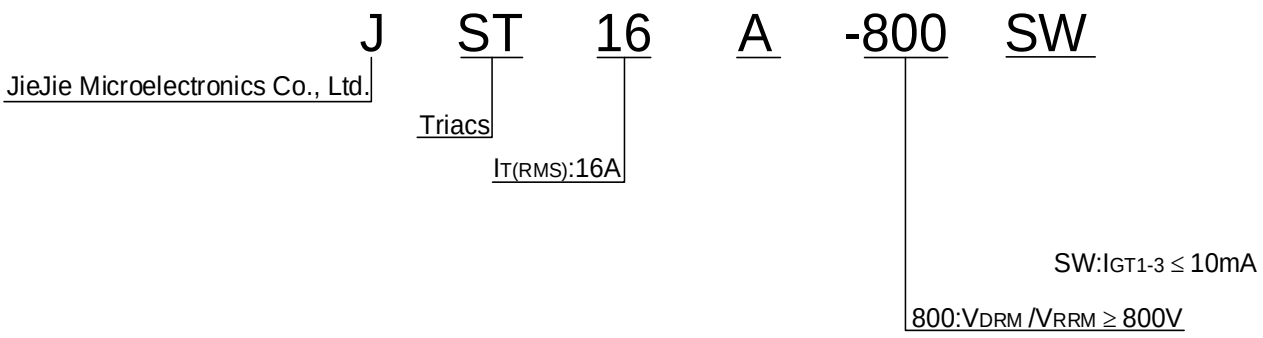


FIG.1: Maximum power dissipation versus RMS on-state current

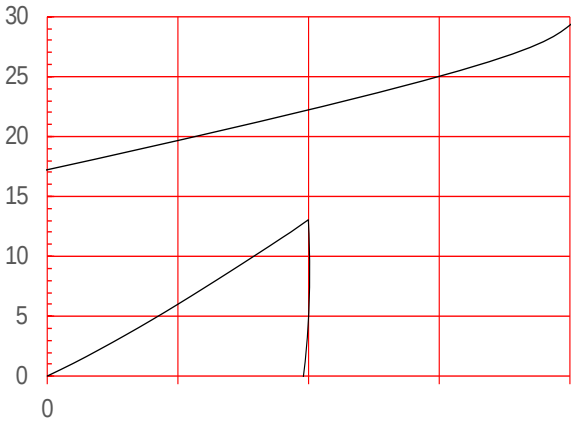
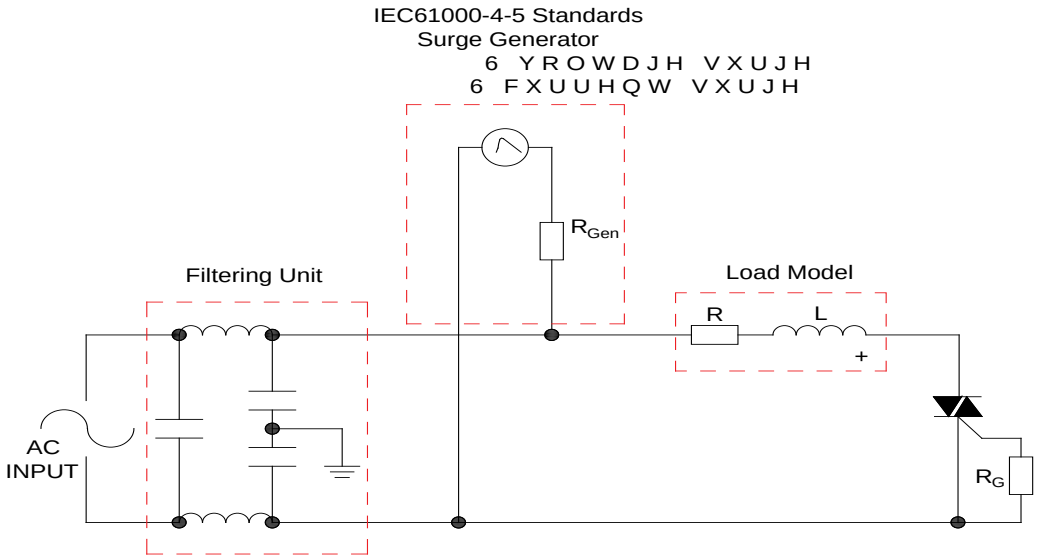


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST16A-800SW	800	10	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last update
Oct.13, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA
Jan.23, 2026	A.1.2	Revise $R_{th(j-c)}$
Sept.15, 2026	A.1.3	Revise I_{TSM}, dV/dt

PACKAGE MECHANICAL DATA