Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3k	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	25	mA
				30	
I_H	I_T =500mA		MAX.	15	mA
dV/dt	V_D=400V Gate Open T_j =125		MIN.	1500	V s
(dI/dt)c	(dV/dt)c=1 9 V_j=125		MIN.	5	A/ms
t_{on}	I_G=20mA I_A=200mA I_R=20mA T_j=25		TYP.	5	s
t_{off}				30	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX .)	Unit
V_{TM}	I_{TM} =22.5A t_p=380 s	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.77	V
R_D	Dynamic resistance	T_j=125	30	P
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	5	A
I_{RRM}		T_j=125	0.4	mA

THERMAL RESISTANCES

ORDERING INFORMATION

J ST 16 E -600 SW -/

- J: JieJie Microelectronics Co., Ltd.
- ST: Triacs
- 16: $I_{T(RMS)}$:16A
- E: E:TO-263
- -600: 600:V_{DRM} /V_{RRM} 600V
- SW: SW:I_{GT1-3} 10mA
- -/: Blank:Tube; -TR:Tape &Reel

MARKING

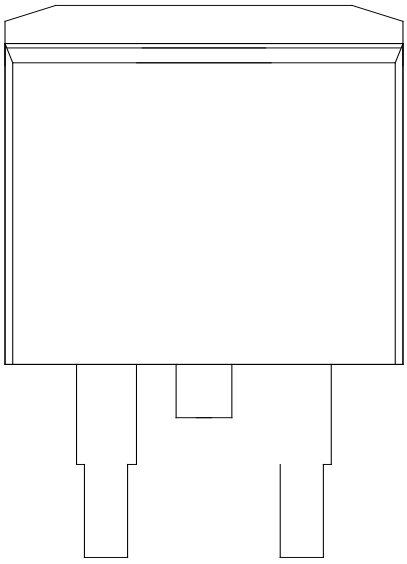
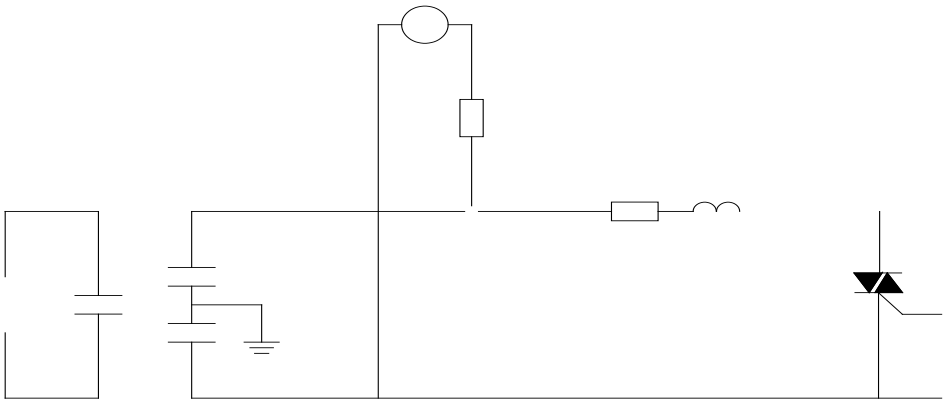


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



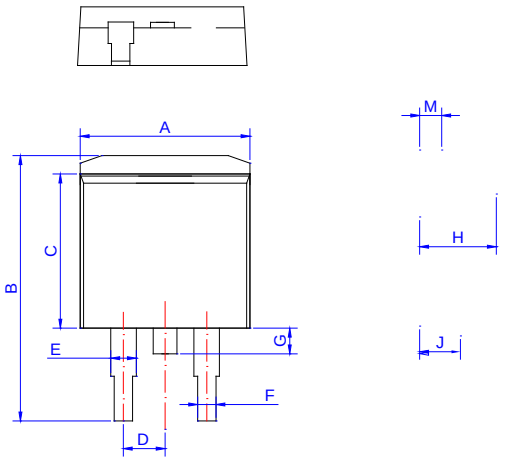
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST16E-600SW	600	10	TO-263	50	Tube
JST16E-600SW-TR				800	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.20, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA
Sept.15, 2026	A.1.2	Revise I_{TSM}, dV/dt

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.40		9.60	0.370		0.378
D	2.40			0.094		
E	1.20		1.50	0.047		0.059
F	0.75		0.85	0.029		0.033

Information furnished in this document is believed to be accurate and reliable. However,