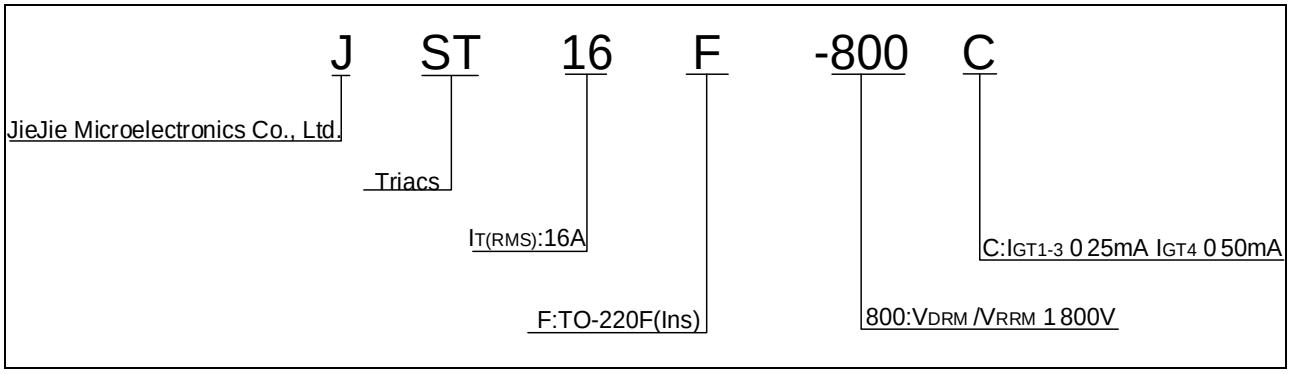
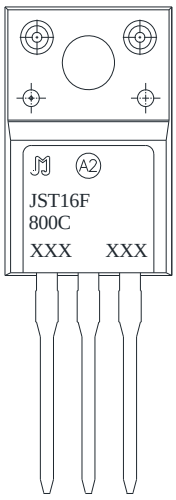
	P_{GM}	10	W
	V_{pp}	3.5	kV

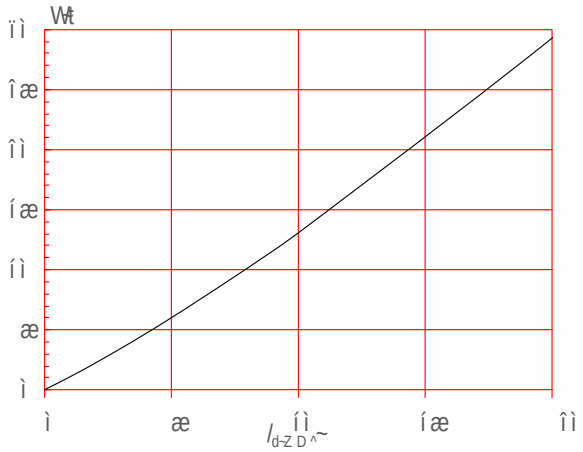
ORDERING INFORMATION



MARKING



Maximum power dissipation versus RMS on-state current



RMS on-state current versus case temperature

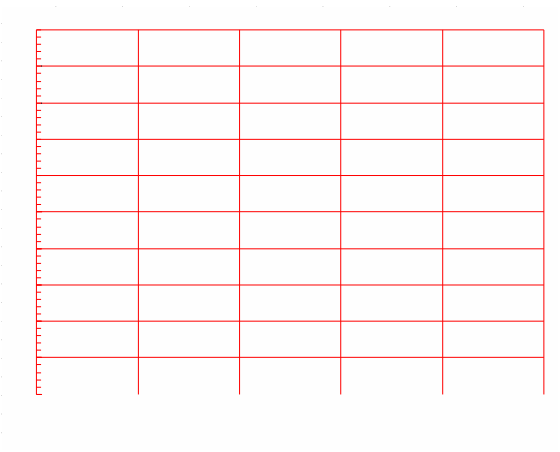
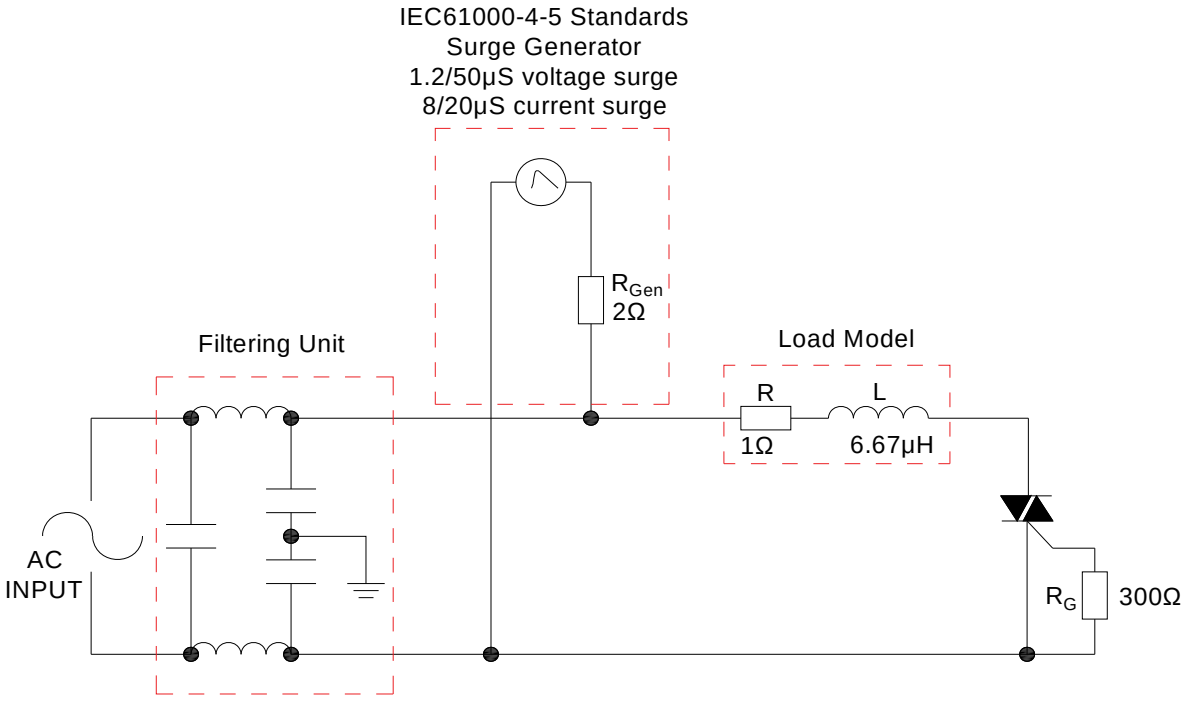
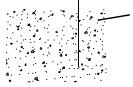


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

PACKAGE MECHANICAL DATA



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