Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV

J ST 16 X -800 BW

- J: JieJie Microelectronics Co., Ltd.
- ST: Triacs
- 16: $I_{T(RMS)}$:16A
- X: X:TO-220F(Ins)
- -800: 800:V_{DRM} /V_{RRM} ≥ 800V
- BW: BW:I_{GT1-3} ≤ 50mA

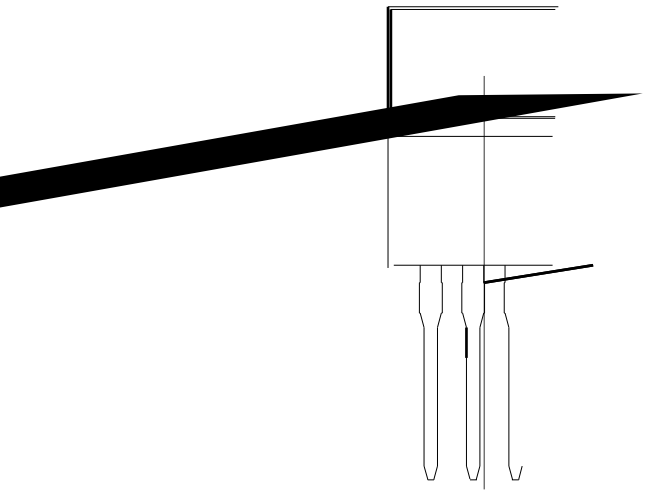


FIG.1: Maximum power dissipation versus RMS on-state current

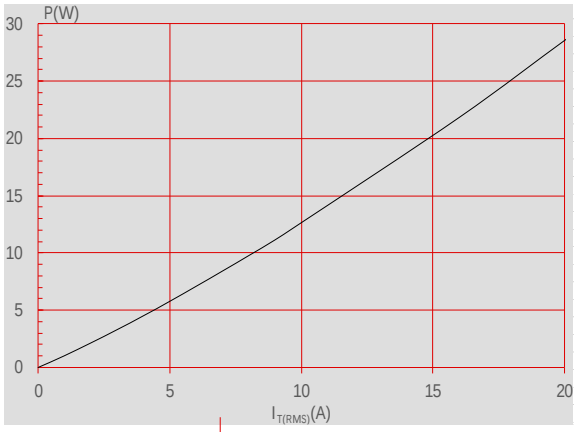


FIG.2: RMS on-state current versus case temperature

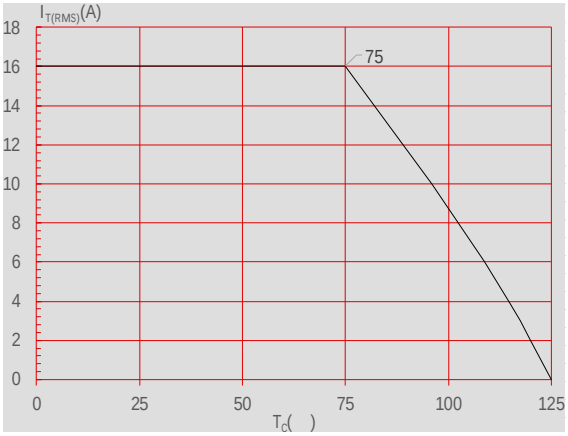


FIG.3: Surge peak on-state current versus number of cycles

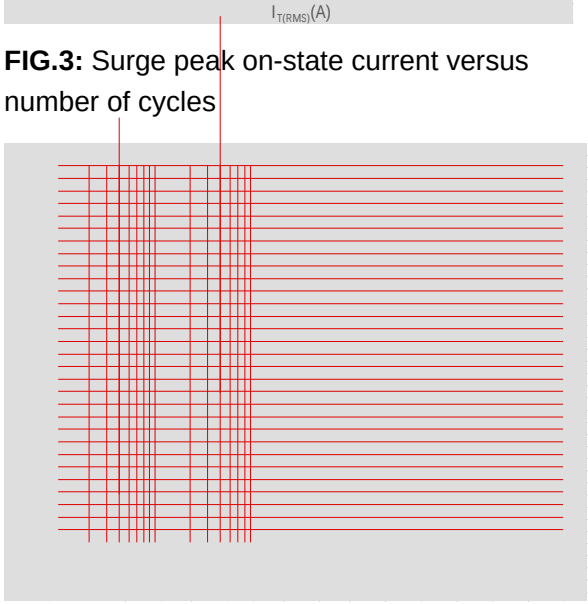
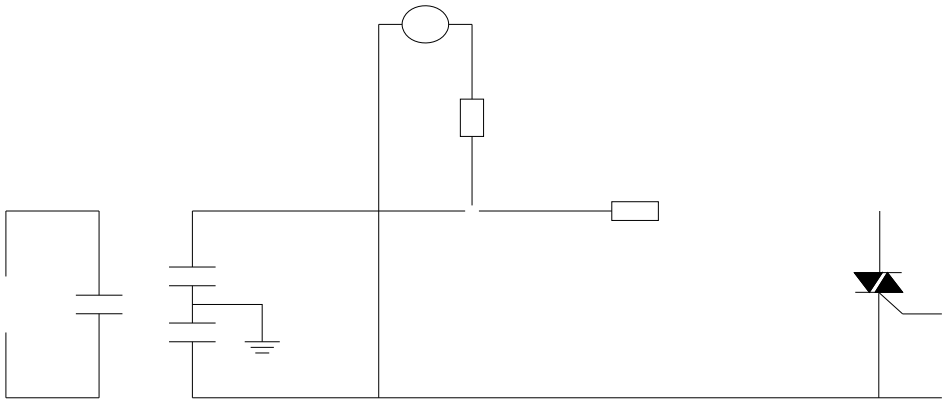


FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST16X-800BW	800	50	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

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