Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	50	mA
				60	
I_H	I_T =500mA		MAX.	40	mA
dV/dt	V_D=400V Gate Open T_j =125		MIN.	2000	V/μs
(dI/dt)c	(dV/dt)c=20V/μs T_j=125		MIN.	20	A/ms
t_{on}	I_G=40mA I_A=200mA I_R=20mA T_j=25		TYP.	7	μs
t_{off}				50	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =28A t_p=380μs	T_j=25	1.4	V
V_{TO}	Threshold voltage	T_j=125	0.77	V
R_D	Dynamic resistance	T_j=125	20	mΩ
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	0.8	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	2.2	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W

FIG.1: Maximum power dissipation versus RMS on-state current

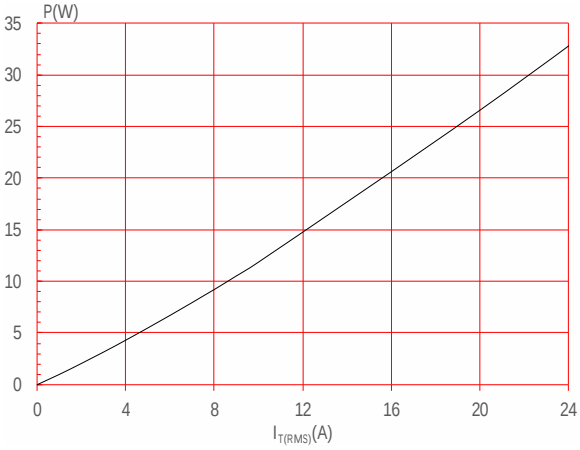
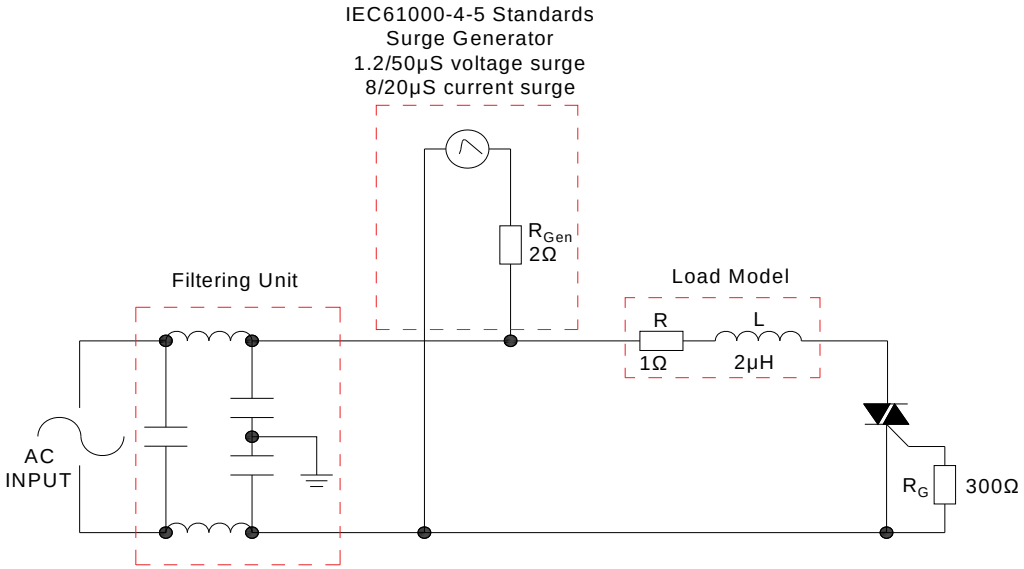


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics.

PACKAGE MECHANICAL DATA



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