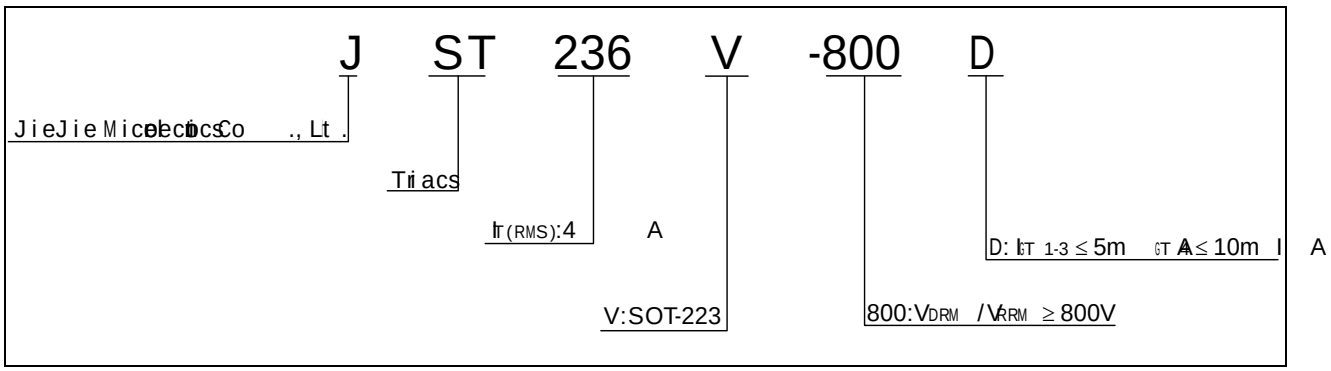
(TJ=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
I_T	V_D=12V R_L=33Ω	- -	MAX	


J
ST
236
V
-800
D
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):4
A
V:SOT-223
800:VDRM /VRRM ≥ 800V
D: IGT 1-3 ≤ 5m GT A≤ 10m I
A

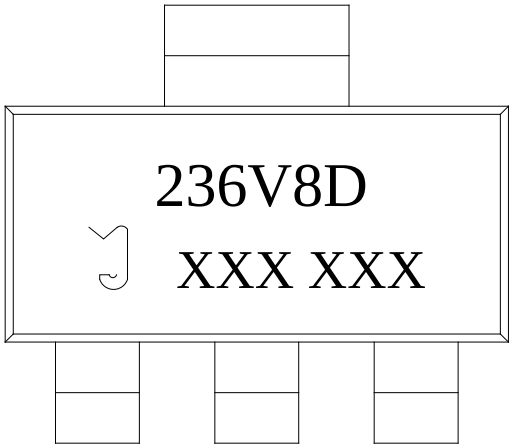

236V8D
XXX XXX

FIG.1: Maximum permissible RMS on-state current vs. case temperature

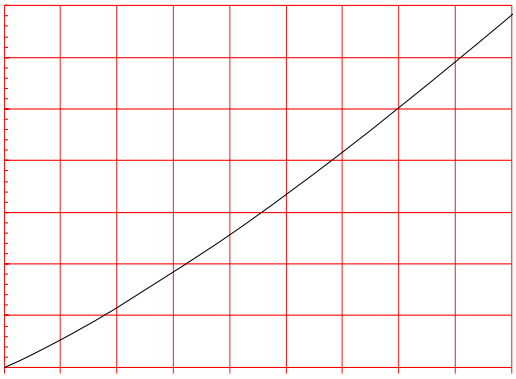


FIG.2: RMS on-state current vs. case temperature

FIG.7: Relative variation of gate trigger current, holding current and latching current versus junction temperature

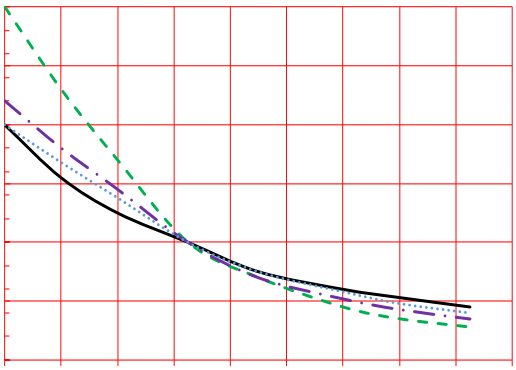
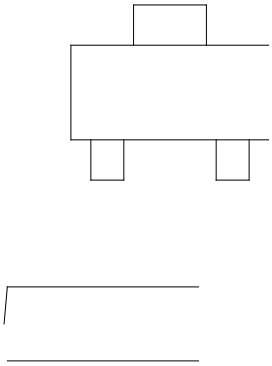


FIG.8 Test circuit for inductive and resistive loads IEC -61000-4-5 surge



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)			Package	Base qty. (pcs)	Delivery mode
		-	-				



Ref.	Dimensions					
	Millimeters			Inches		
	Min	Typ	Max	Min	Typ	Max
	A1.50	1.60	1.80	0.059	0.063	0.071
	A0.01	1 0.06	0.10	0.001	0.002	0.004
	2.90	3.00	3.10	0.114	0.118	0.122
	0.60	0.70	0.80	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.30	6.50	6.70	0.248	0.256	0.264
E	3.30	3.50	3.70	0.130	0.138	0.146
F	4.40			0.173		
F1	2.20			0.087		
G	0.50		1.00	0.020		0.039
H	1.50	1.75	2.00	0.059	0.069	0.079
J	6.70	7.00	7.30	0.264	0.276	0.287
K						

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