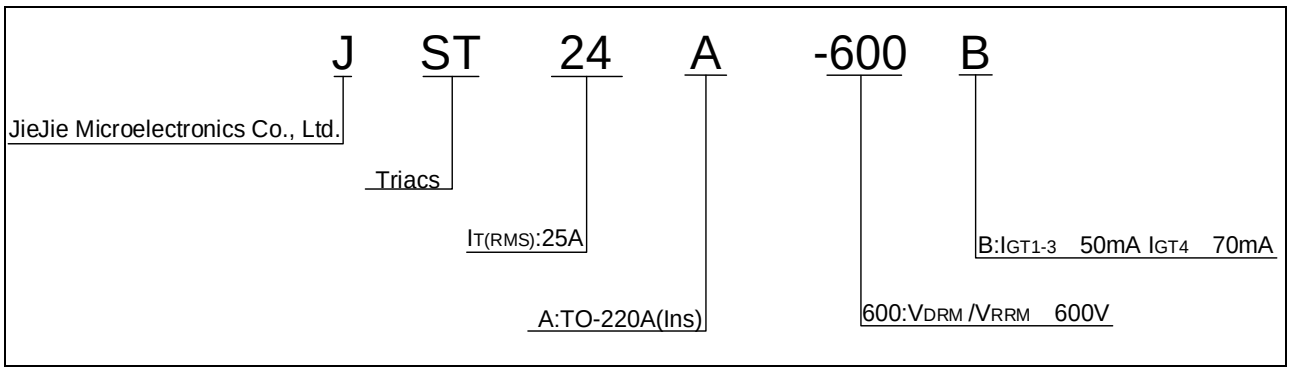
JST24A-600B 25A TRIAC

Rev.A.1.1

Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	2	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	- -	MAX.	50	mA
				70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	V_D				


J
ST
24
A
-600
B
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):25A
A:TO-220A(Ins)
600:VDRM /VRRM 600V
B:IGT1-3 50mA IGT4 70mA

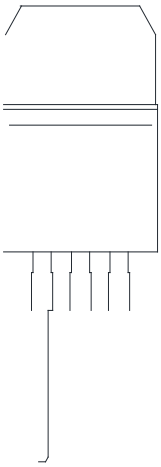


FIG.1: Maximum power dissipation versus RMS on-state current

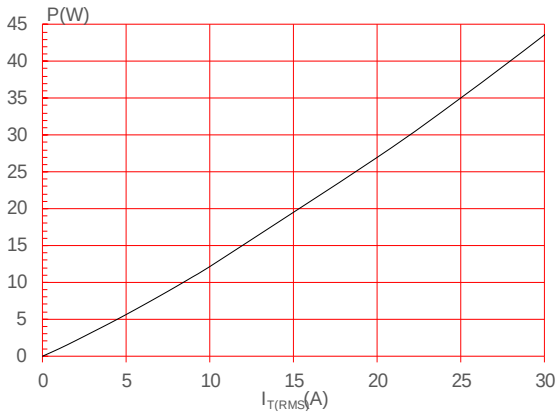


FIG.2: RMS on-state current versus case temperature

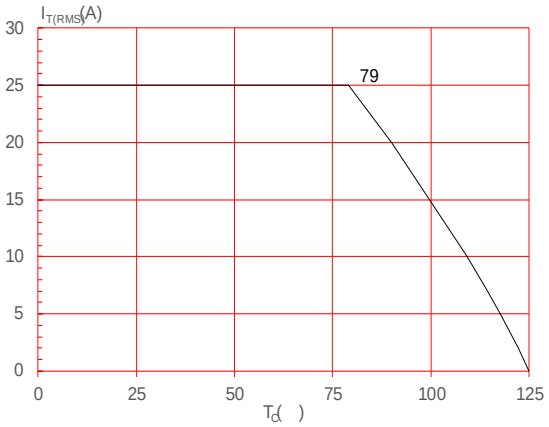
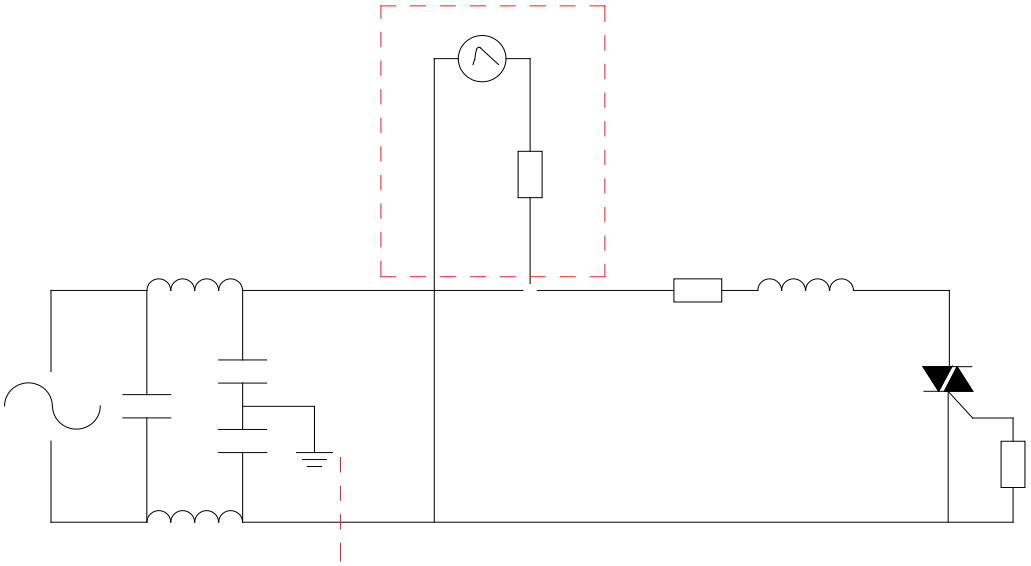


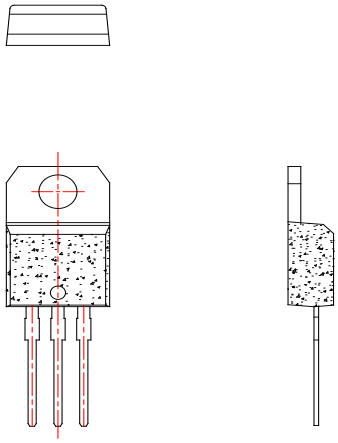
FIG.3: Surge peak on-state current versus number of cycles

FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST24A-600B	600	50	70	TO-220A(Ins)	50	Tube



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