JST30Z-1200CW 30A TRIAC

Rev.A.1.1

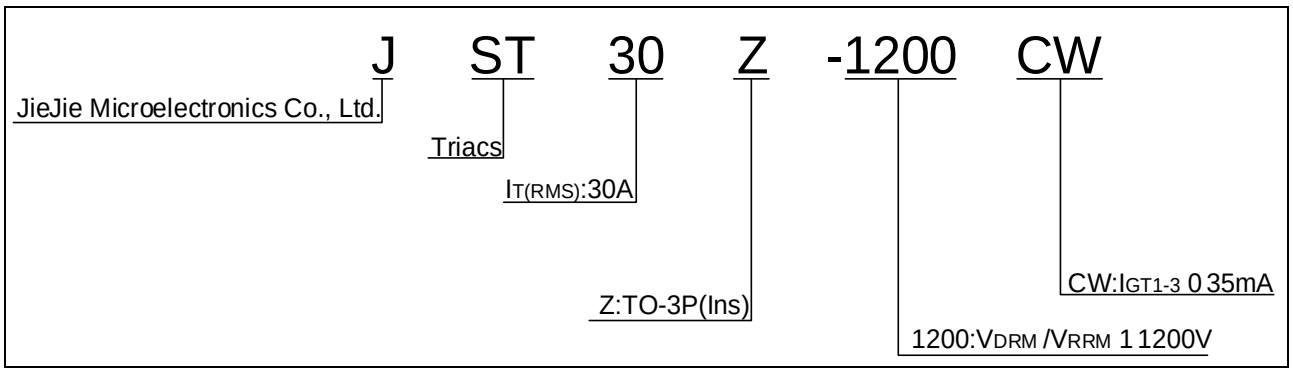
DESCRIPTION:

The JST30Z-1200CW triac is s

Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	2.5	kV

ELECTRICAL CHARACTERISTICS(T_j=25 unless otherwise specified)

ORDERING INFORMATION



MARKING

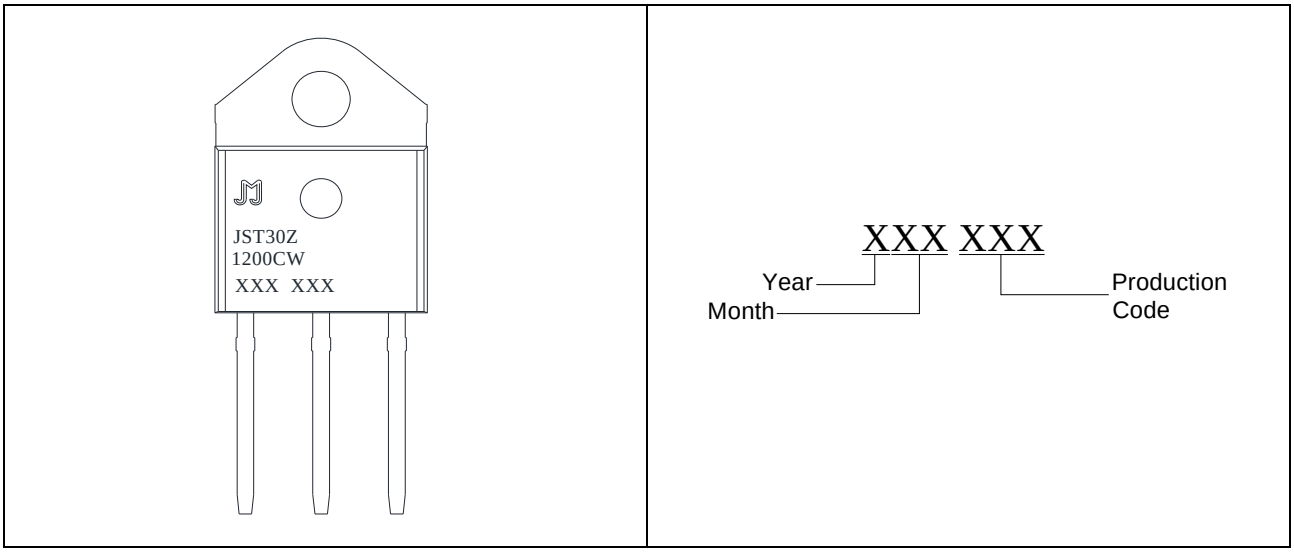
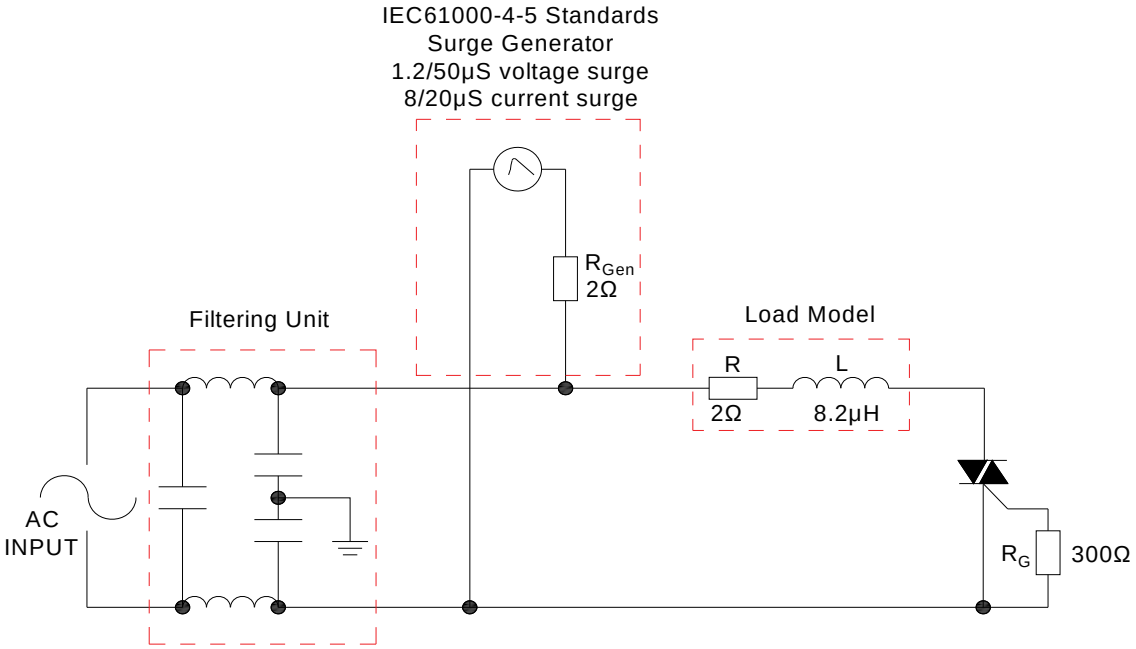


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie D] Œ } o š Œ } v] •

JST30Z-1200CW

PACKAGE MECHANICAL DATA



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