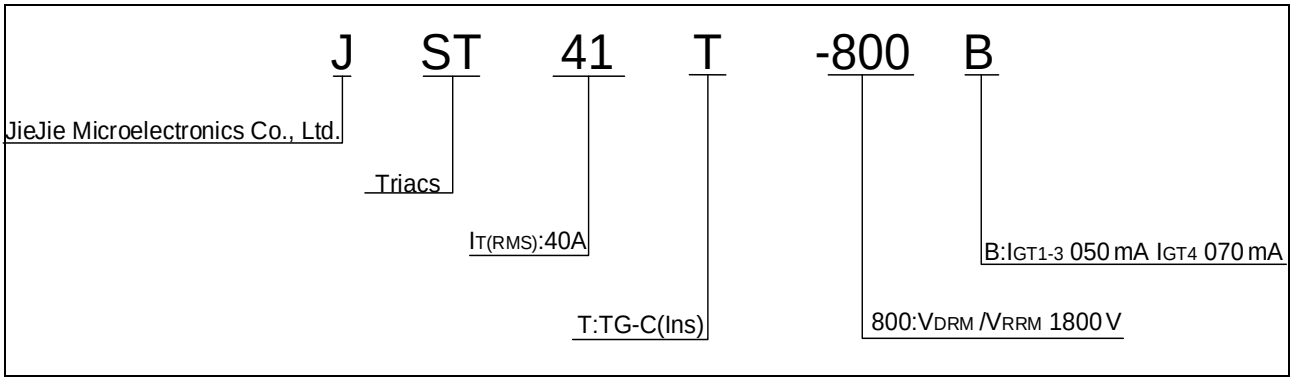
| | | | |
|---|---|---|---|
| Peak pulse voltage<br>($T_j$=25 ; non-repetitive,off-state;FIG.7) | $V_{pp}$ | 0.9 | kV |

ELECTRICAL CHARACTERISTICS ($T_j$=25 unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | | Unit |
|---|---|---|---|---|---|
| $I_{GT}$ | $V_D$=12V $R_L$=33 | - - | MAX. | 50 | mA |
| | | | | 70 | |

V

## ORDERING INFORMATION




## MARKING

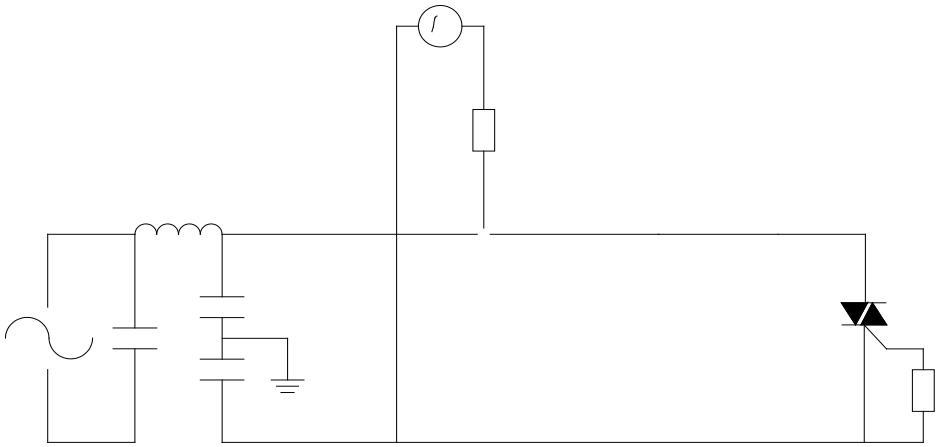


FIG.1: Maximum power dissipation versus RMS on-state current



FIG.2: RMS on-state current versus case temperature

FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



## ORDERING INFORMATION

| Order code | Voltage $V_{DRM}/V_{RRM}$ (V) | IGT(mA) | | Package | Base qty. (pcs) | Delivery mode |
|---|---|---|---|---|---|---|
| | | H- I- J | K | | | |
| JST41T-800B | 800 | 50 | 70 | TG-C(Ins) | 10 | Tube |

Document Revision History

| Date | Revision | Changes |
|---|---|---|
| Apr.14, 2023 | A.1.0 | Last updated |

PACKAGE MECHANICAL DATA



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,