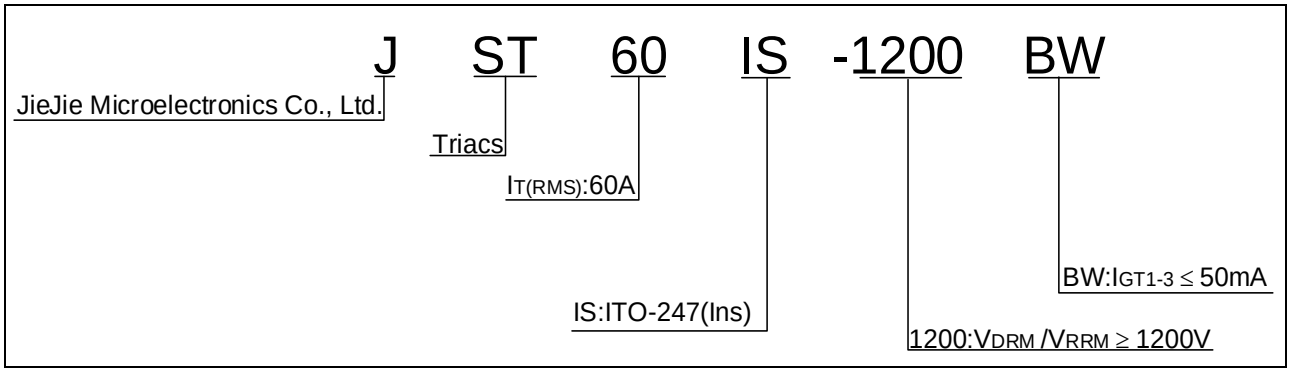
Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	25	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	1.1	kV

ELECTRICAL CHARACTERISTICS(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33q	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1.3	V
V_{GD}	V_D =V_{DRM} T_j=125 R_L =3.3kq	- -	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	-			

ORDERING INFORMATION



MARKING

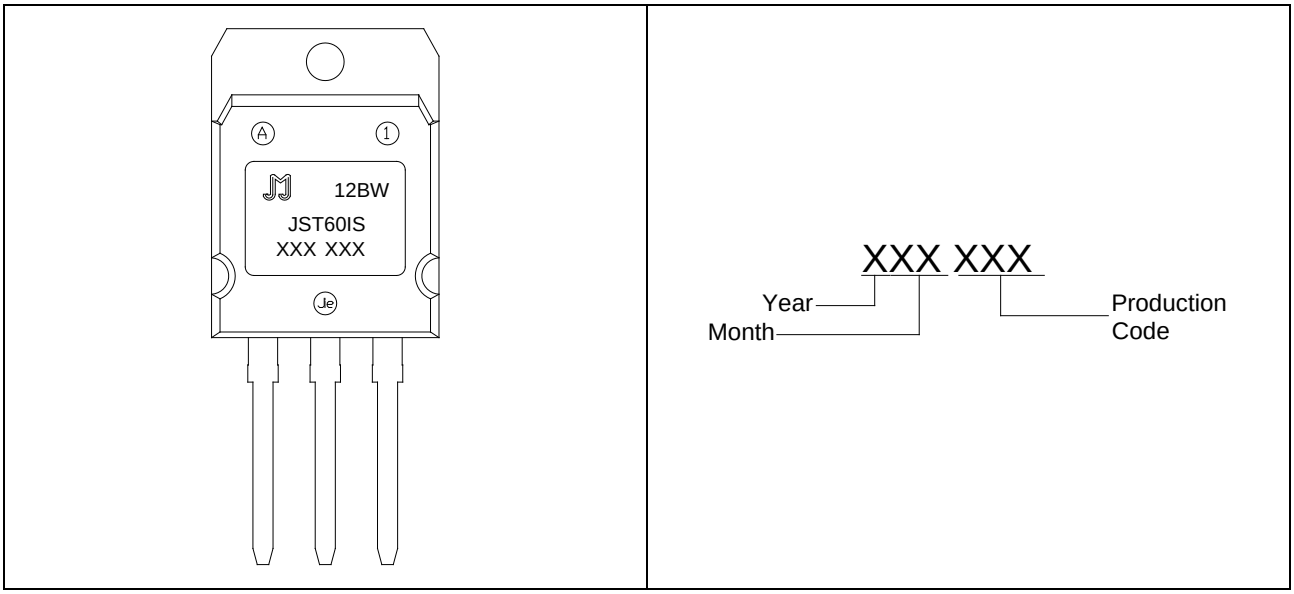


FIG.1: Maximum power dissipation versus RMS on-state current

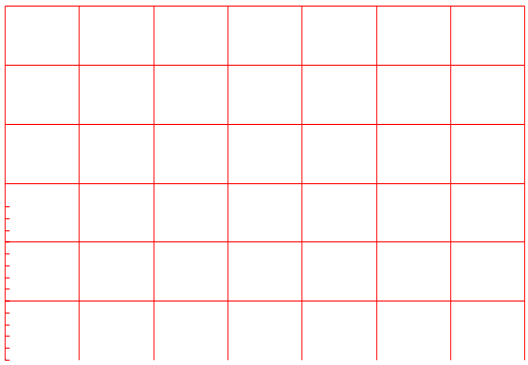


FIG.2: RMS on-state current versus case temperature

FIG.7

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM}(V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST60IS-1200BW	1200	50	ITO-247(Ins)	25	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA

