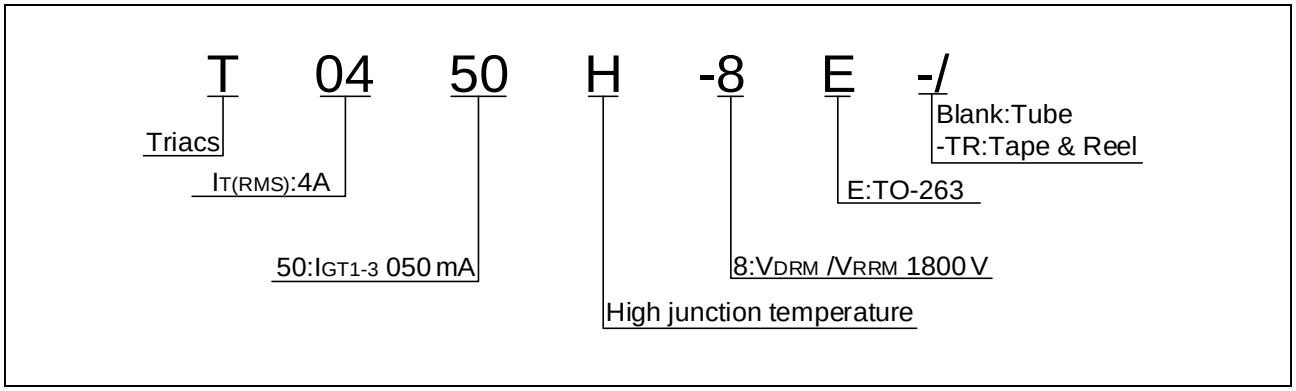
T0450H-8E 4A TRIAC

Rev.A.1.0

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ORDERING INFORMATION



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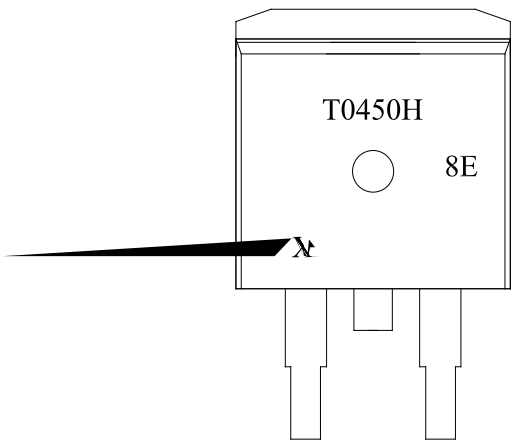


FIG.1 Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

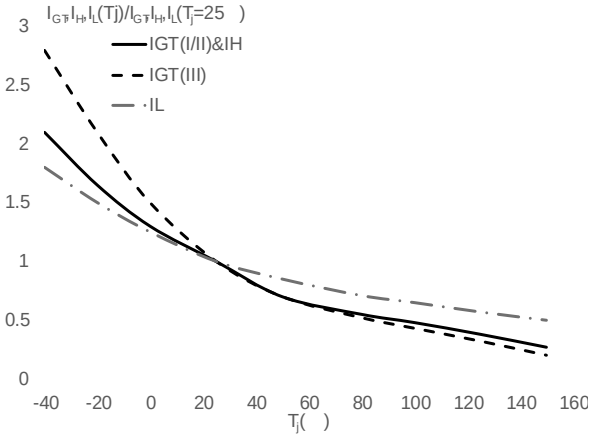
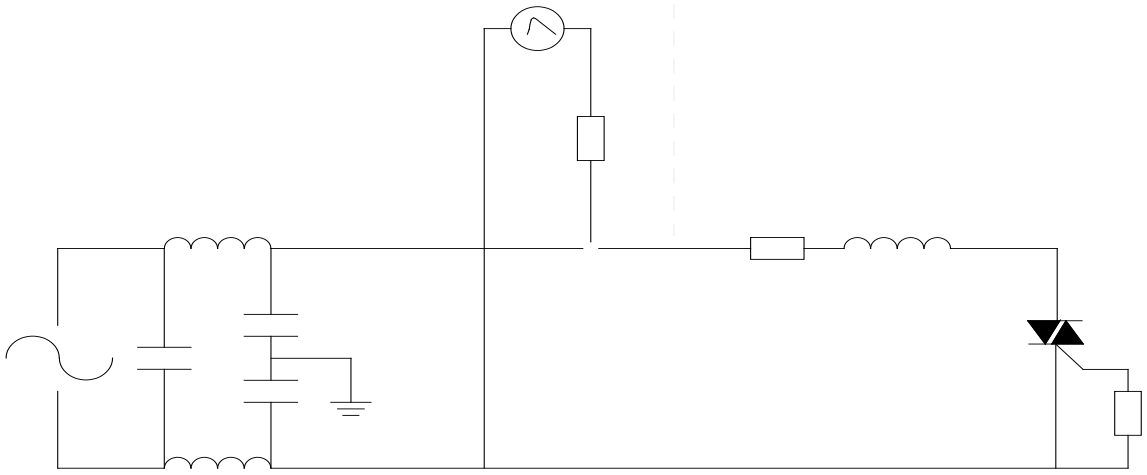


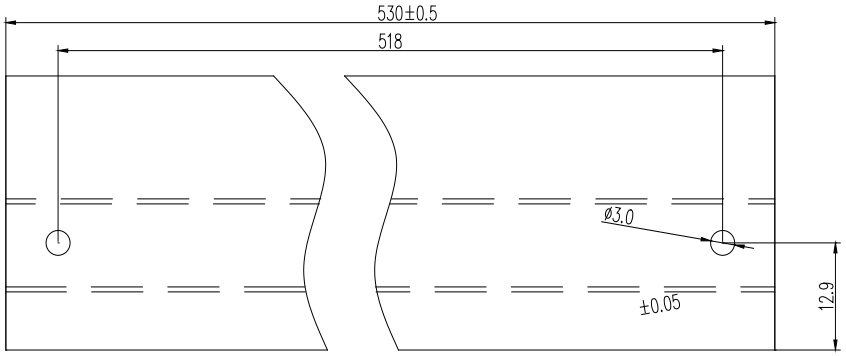
FIG.8 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards

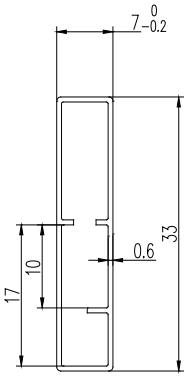


ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	

DELIVERY MODE





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