Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	4.5	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	20	mA

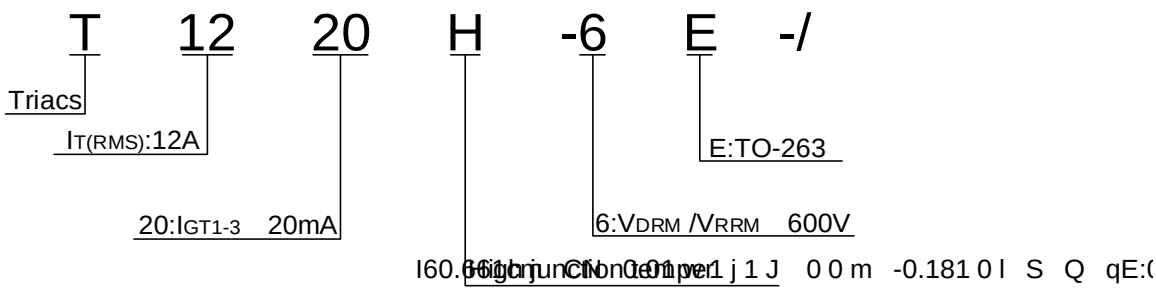

T 12 20 H -6 E -/
Triacs
IT(RMS):12A
20:IGT1-3 20mA
E:TO-263
6:VDRM /VRRM 600V

FIG.1 Maximum power dissipation versus RMS on-state current

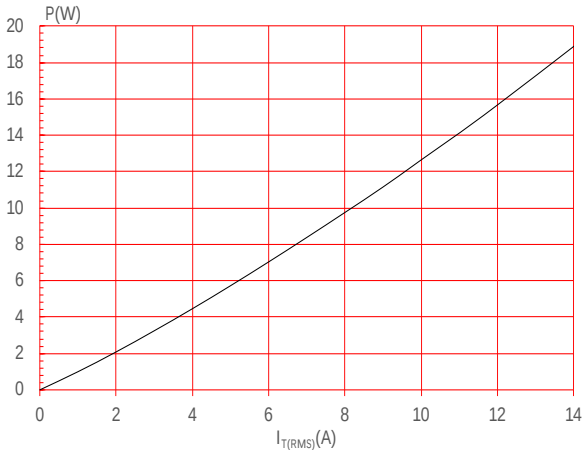


FIG.2: RMS on-state current versus case temperature

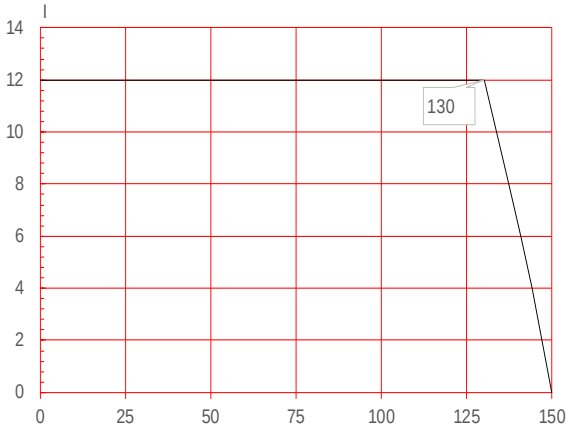


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

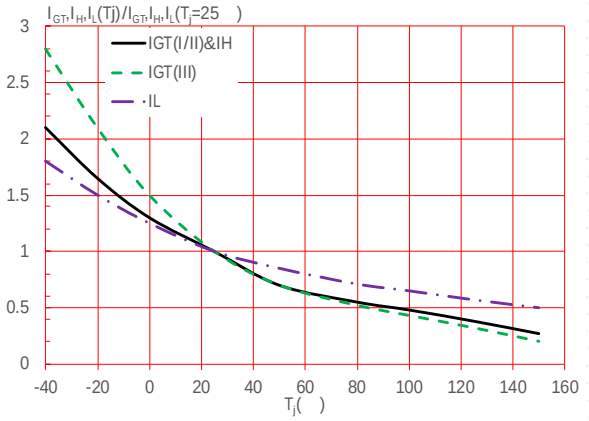
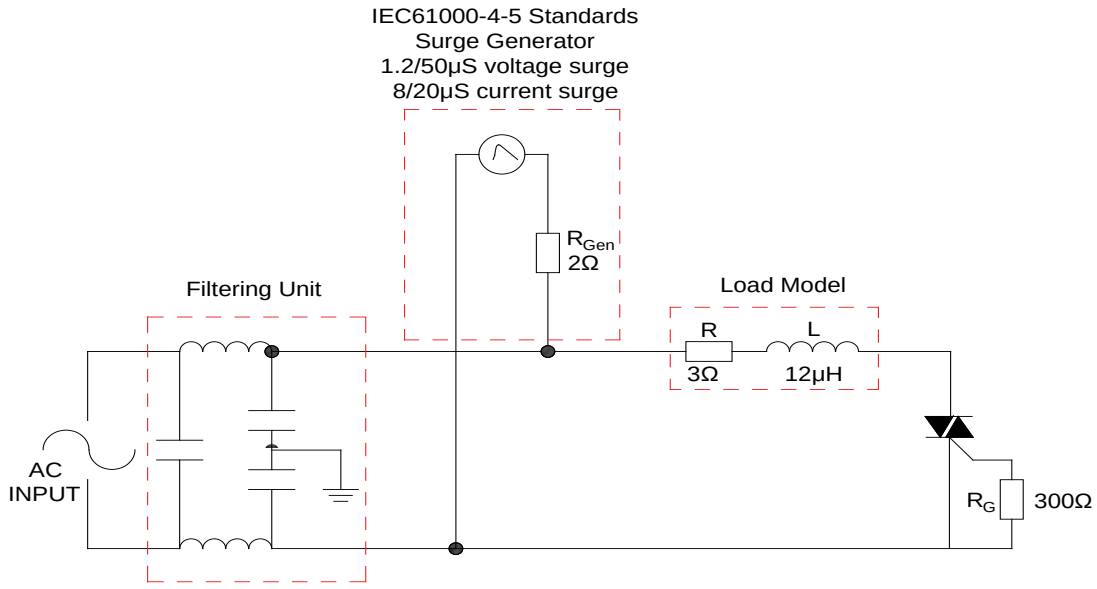
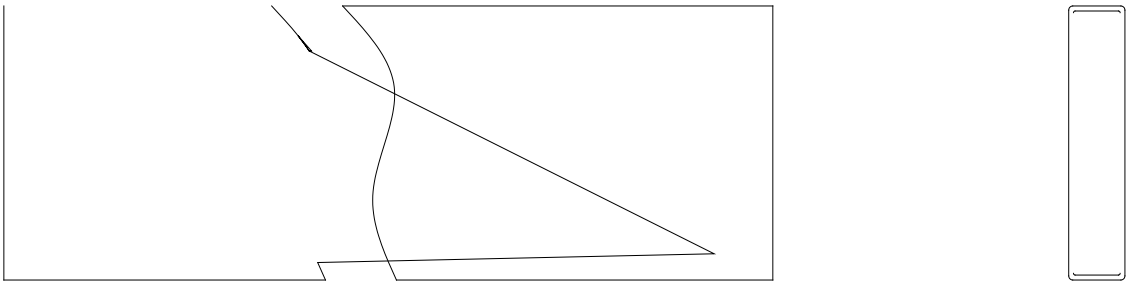


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T1220H-6E	600	20	TO-263	50	Tube
T1220H-6E-TR				800	Tape & Reel



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