Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.8)	V_{pp}	4	kV

(T_j=25 unless otherwise specified)

Symbol Test Condition Quadrant

T 16 50 H -6 E -/

- T: Triacs
- 16: $I_{T(RMS)}$:16A
- 50: $I_{GT1-3} \leq$ 50mA
- H: High junction temperature
- -6: V_{DRM} /$V_{RRM} \geq$ 600V
- E: TO-263
- -/: Blank:Tube; -TR:Tape & Reel

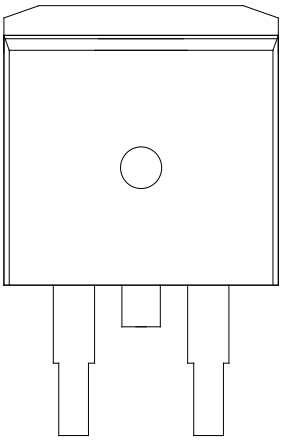


FIG.1 Maximum power dissipation versus RMS on-state current

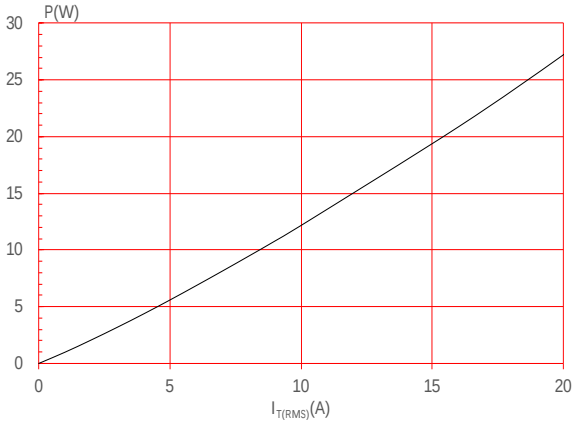


FIG.2: RMS on-state current versus case temperature

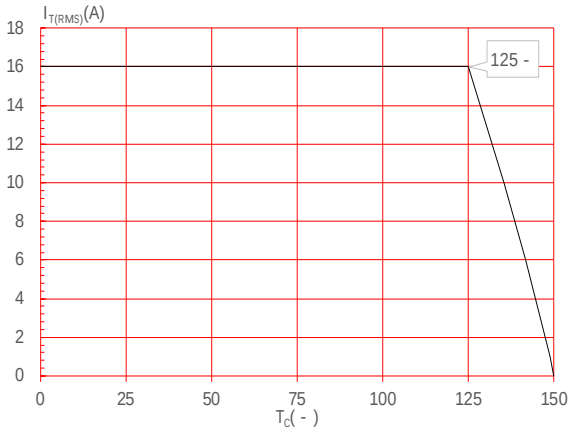
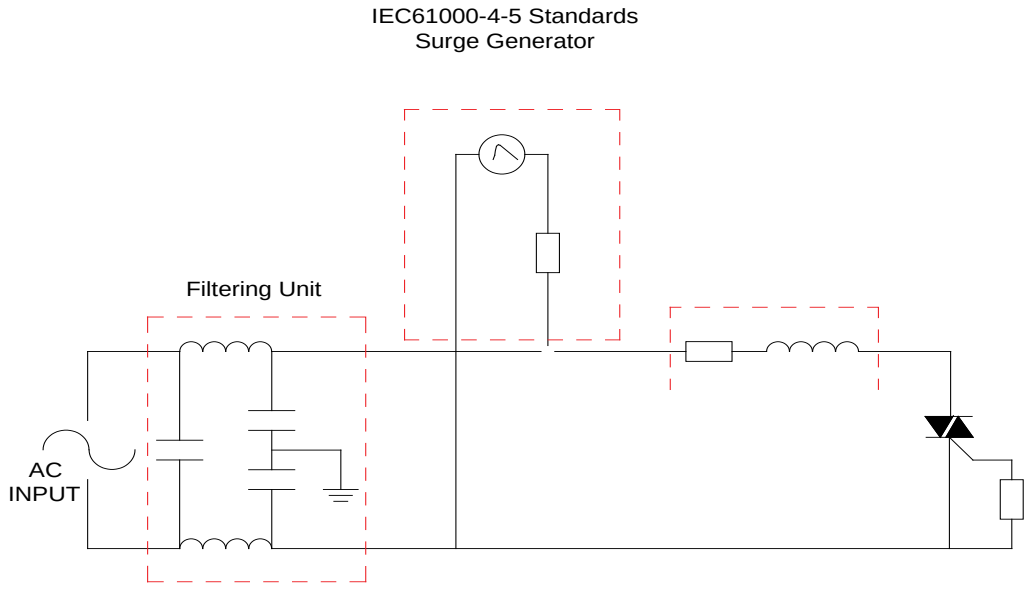
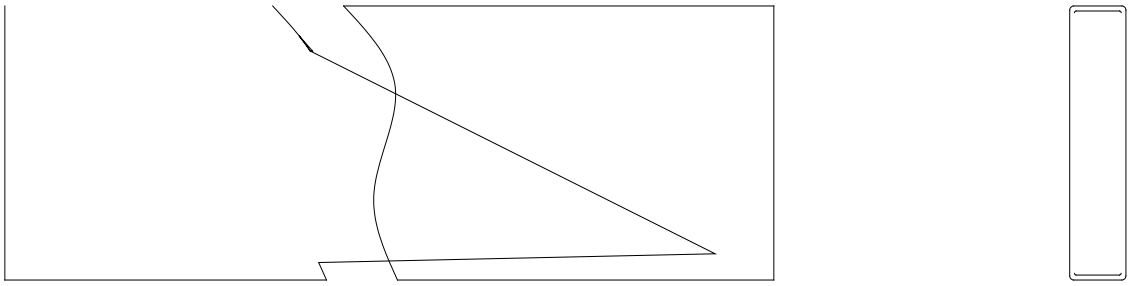


FIG.7:

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





Information furnished in this document is believed to be accurate and reliable. However,