(&

- Low forward voltage and leakage current
- Low inductance package
- High surge current capability

&

- Field supply for DC motors
- Line rectifiers for transistorized AC motor controllers
- Non-controllable rectifiers for AC/DC converter

(@ T_C = 25℃ unless otherwise specified)

((			(	
Repetitive peak reverse voltage		V_{RRM}	1600	V
Non-repetitive peak reverse voltage		V_{RSM}	1700	V
Output current	T_C = 100	I_D	130	A
Forward surge current	1/2 cycle, Sine wave, 50Hz T_{vj} = 25	I_{FSM}	1400	A
I^2t value for fusing			I^2tg (e	

All product information is copyrighted and subject to legal disclaimers

(& & & (& (@ T_C = 25℃ unless otherwise specified)

((			(			
Forward voltage	I_D = 130A, Tvj = 25	V_F		1.20	1.40	V
Reverse leakage current	V_R = V_{RRM}, Tvj = 25	I_{RRM}			0.5	mA
	V_R = V_{RRM}, Tvj = 150				10	mA
Threshold voltage	For power loss calculation only Tvj = 150 ,	V_{TO}			0.87	V
Dynamic resistance		r_T			3.5	mΩ

(& (& (@ T_C = 25℃ unless otherwise specified)

((			(			
Thermal impedance (junction to case)	per diode	$R_{th(j-c)}$			0.85	K/W
Mounting torque	Base plate to heatsink screw M6	M	4.25		5.75	N m
	Electrode to terminal screw M6		4.25		5.75	N m

(

(&		& (	(	(	(
JMD130TD16D2H	JMD130TD16D2H	D2H	240g/PCS	4 PCS	36 PCS

All product information is copyrighted and subject to legal disclaimers

& (& & (&(&

FIG.1: Forward characteristics(per diode)

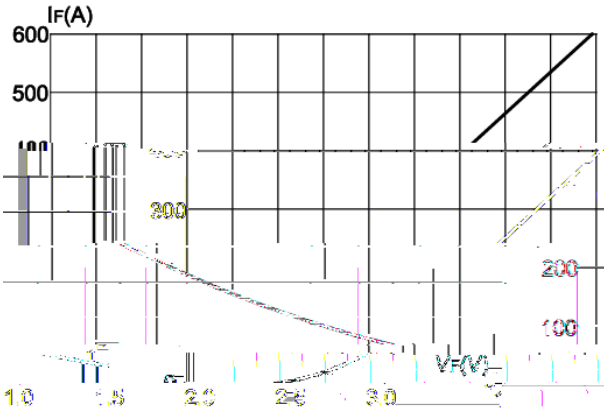
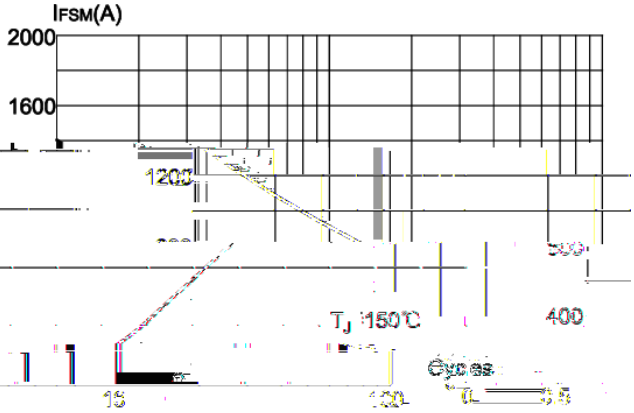
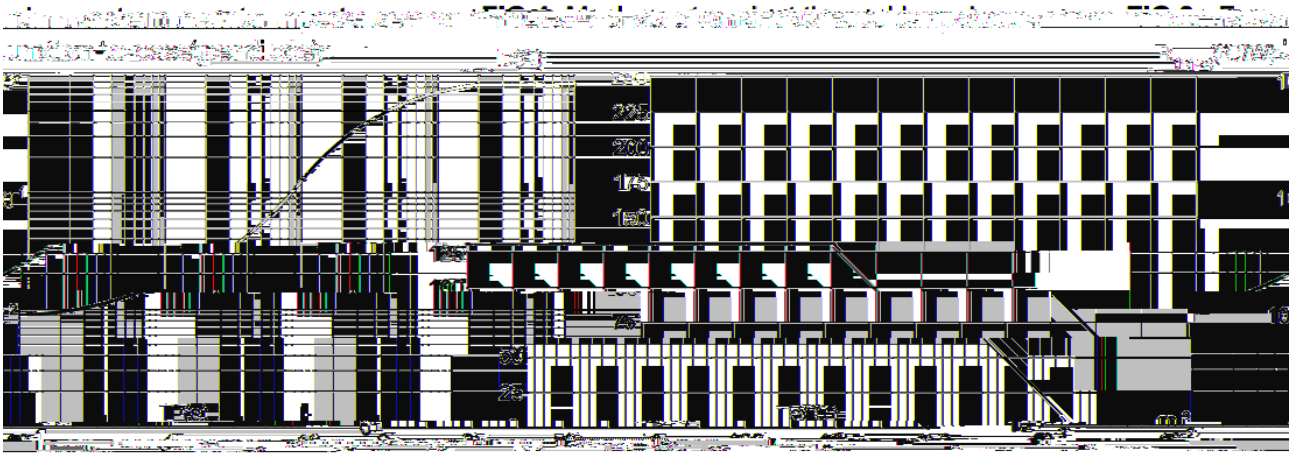
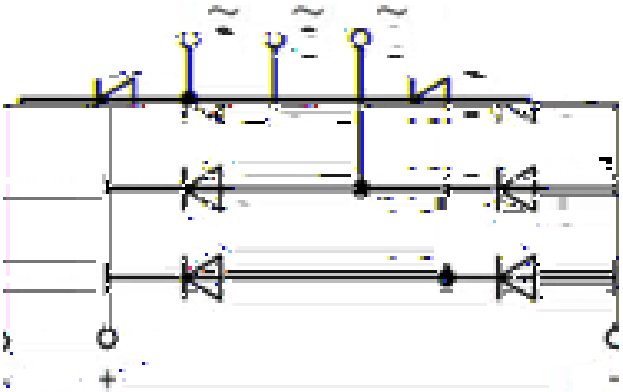


FIG.2: Peak on-state surge current





&



All product information is copyrighted and subject to legal disclaimers



Unit of measurement: mm

All product information is copyrighted and subject to legal disclaimers

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Semiconductor Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. This information in this document is subject to change without prior notice. Notwithstanding this, Jiangsu JieJie will fully comply with the terms outlined in a signed agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document is the initial release, dated 20 July 2026. This document supersedes and replaces all information previously supplied.

is registered trademark of Jiangsu JieJie Semiconductor Co., Lt ©2026 Jiangsu JieJie Semiconductor Co., Ltd. All rights reserved.